

How Hard is Weak Memory Testing?

S. Chakraborty, S. Krishna, U. Mathur and **Andreas Pavlogiannis**



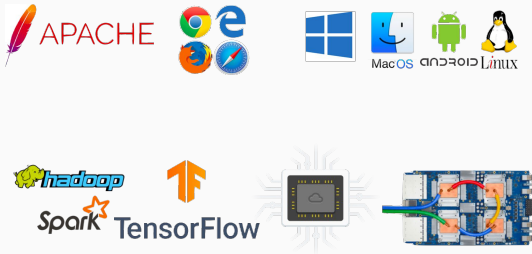
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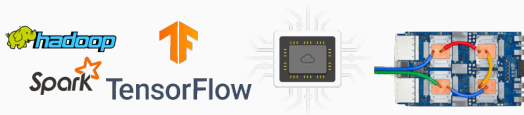
Concurrency is Everywhere

Concurrency is a ubiquitous computing paradigm



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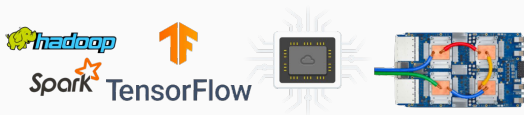


Verification:

- Is my program correct in a given concurrent setting?
- What behaviors are possible in a concurrent environment?

Concurrency is Everywhere

Concurrency is a ubiquitous computing paradigm



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Not all concurrency is the same

Store Buffer under x86-TSO

Store Buffer

$x = 0, y = 0$

$w(y, 1);$	\parallel	$w(x, 1);$
$a := r(x, 0)$	\parallel	$b := r(y, 0)$

Not sequentially consistent

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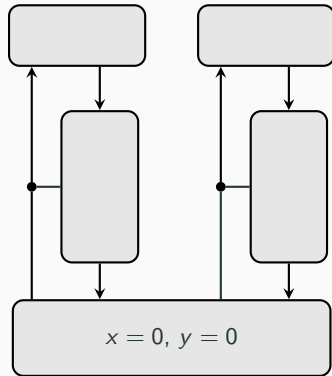
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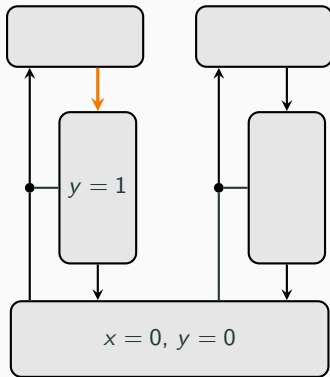
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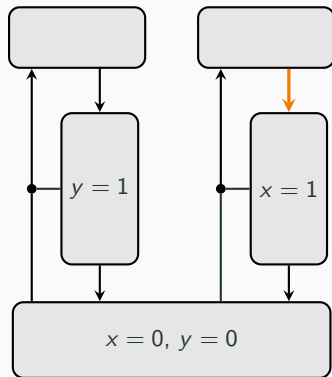


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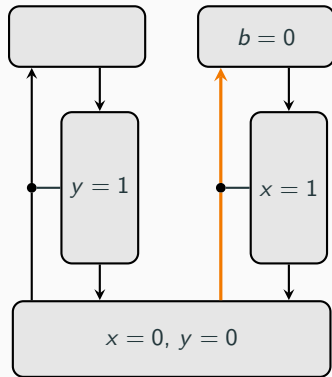
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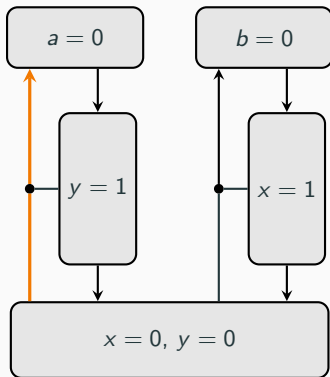


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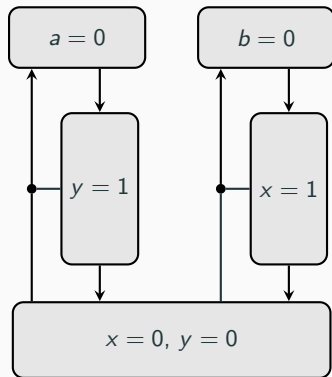


Store Buffer under x86-TSO

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- Behavior possible under x86-TSO

Memory Models

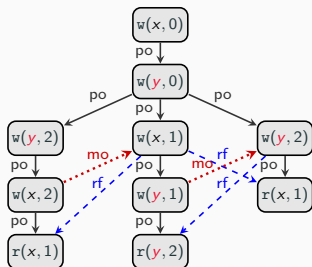
- Formal models of concurrency
- Specifications of all possible communication patterns
 - Buffers/caching
 - Out-of-order execution
 - Speculation
 - Cache coherence protocols
 - Compiler optimizations
 - Message delays
 - ...
- In all cases: **weak data consistency**

Memory Models

SC	Standard Sequential Consistency
TSO	x86-Total Store Order
PSO	Sparc-Partial Store Order
RA	The release-acquire semantics of C11
Relaxed	The relaxed fragment of C11
Relaxed-Acyclic	Relaxed + (po \cup rf)-acyclicity
CC	Causal consistency
CCv	Causal convergence
CM	Causal memory

Execution Graphs

Program executions are represented as execution graphs



Executions on Weak Memory

An **execution** is a tuple $X = (E, \text{po}, \text{rf}, \text{mo})$, where:

- E is a set of events
- po is the *program order* over E , total on each thread
- rf is a *reads-from* relation on $W \times R$
 - $(w, r) \in \text{rf}$ means r reads the value written by w
- mo_x is a total *modification order* over all writes $w(x)$
 - $\text{mo} = \bigcup_x \text{mo}_x$

Consistent Executions

A memory model \mathcal{M} defines a set of axioms that every execution must satisfy

Consistency

If an execution $X = (E, po, rf, mo)$ satisfies all axioms of \mathcal{M} , we say that X is **consistent** in \mathcal{M} , written as $X \models \mathcal{M}$.

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Memory models may be ordered in terms of the behaviors they allow, i.e., the executions they admit

Weak(er) Memory Models

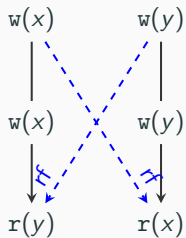
Given two memory models $\mathcal{M}_1, \mathcal{M}_2$, we say that \mathcal{M}_2 is **weaker than** \mathcal{M}_1 , written $\mathcal{M}_1 \sqsubseteq \mathcal{M}_2$, if for every execution X , we have

$$X \models \mathcal{M}_1 \Rightarrow X \models \mathcal{M}_2$$

Eg, $SC \sqsubseteq TSO \sqsubseteq RA \sqsubseteq \{CC, Relaxed\}$

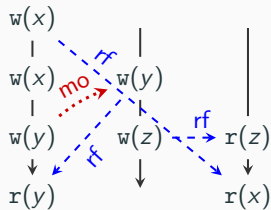
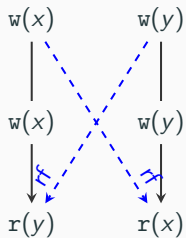
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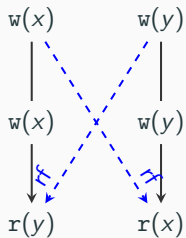
(a) Total Store Order (TSO)

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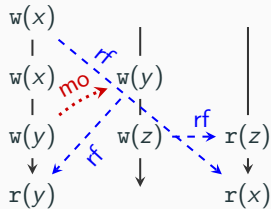


(a) Total Store Order (TSO) (b) Causal Convergence (CCv)

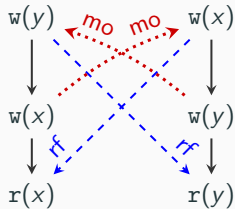
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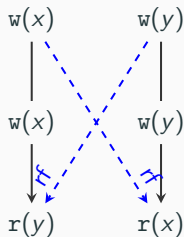


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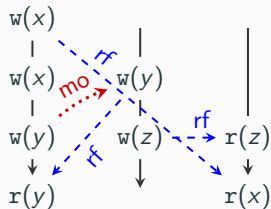


(c) Release/Acquire (RA)

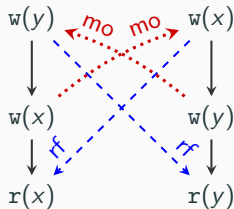
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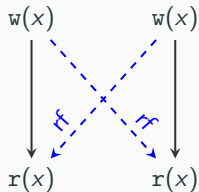
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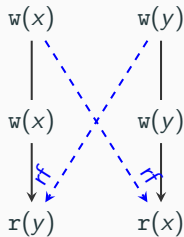


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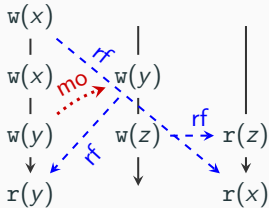


(d) Causal Memory (CM)

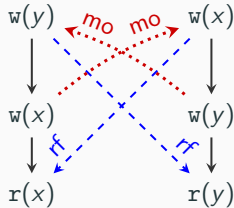
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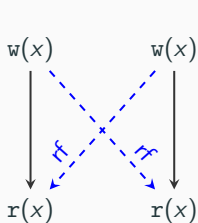
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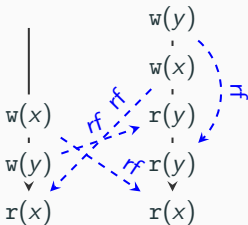
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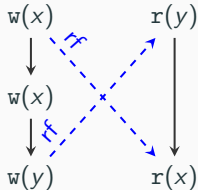
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(e) Causal Consistency (CC)

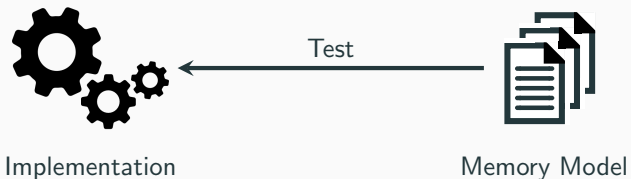


(f) Relaxed (Relaxed)

Testing Weak Memories

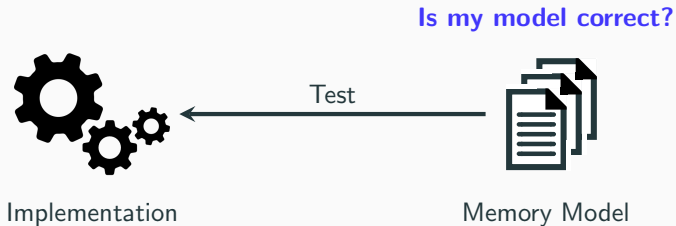
- Aligning a model to an implementation is hard, litmus tests

Is my implementation correct?



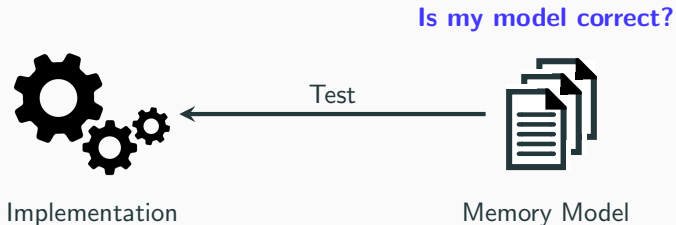
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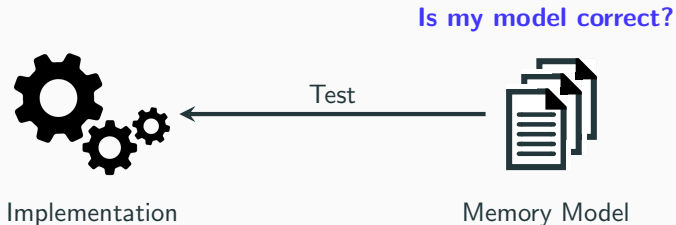
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- Also, model checking, dynamic analyses

Testing Weak Memories

- Aligning a model to an implementation is hard, litmus tests



- Also, model checking, dynamic analyses
- Is the **observed behavior** of the program in alignment with the model?
- Observed behavior is thread-local
 - No **rf**, no **mo**

Weak Memory Testing, Formally

The Testing Problem

Given an abstract execution $\bar{X} = (E, po)$ and a memory model \mathcal{M} is there a reads from relation **rf** and a modification order **mo** such that

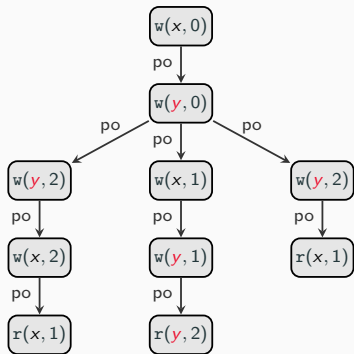
$X = (E, po, \mathbf{rf}, \mathbf{mo}) \models \mathcal{M}$?

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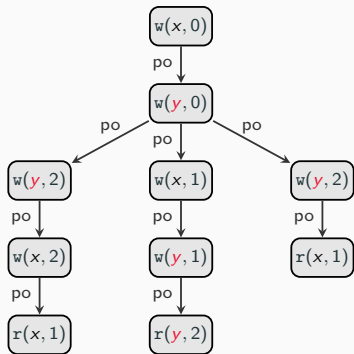
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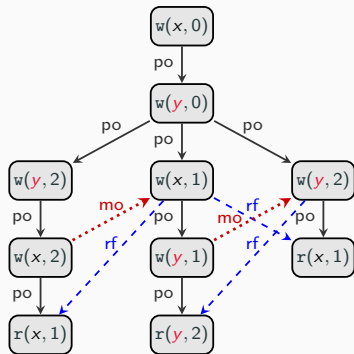
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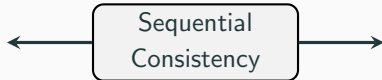
Output X

How Fast can we Test?

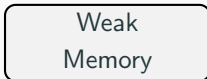
n events, k threads, d memory locations



NP-complete for $k = 3$
NP-complete for $d = 1$



P for $k, d = O(1)$

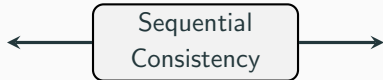


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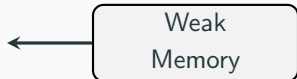
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Sequential
Consistency



P for $k, d = O(1)$



???

Weak
Memory



The Hardness of Weak Memory Testing

Theorem (Hardness of bounded testing)

Testing is NP-hard for any memory model among

- CCv
- RA
- CM
- CC
- Relaxed-Acyclic

even for abstract executions with bounded

- *threads*
- *memory locations*
- *values read/written*

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Non parameterizable!

The Proof is a Bit Involved . . .

Reduction from monotone 1-in-3 SAT $\phi = C_1 \wedge C_2 \wedge \dots \wedge C_m$

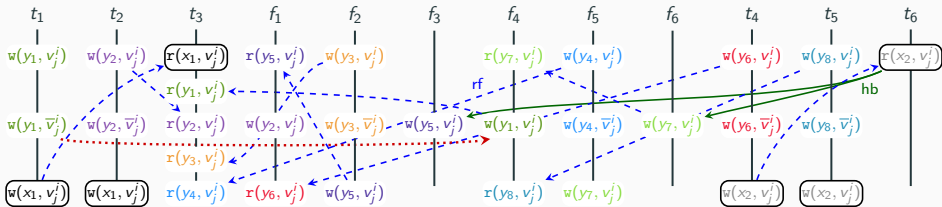
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A Copy Gadget:



How General is this Hardness?

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Theorem

For **any** memory model \mathcal{M} with

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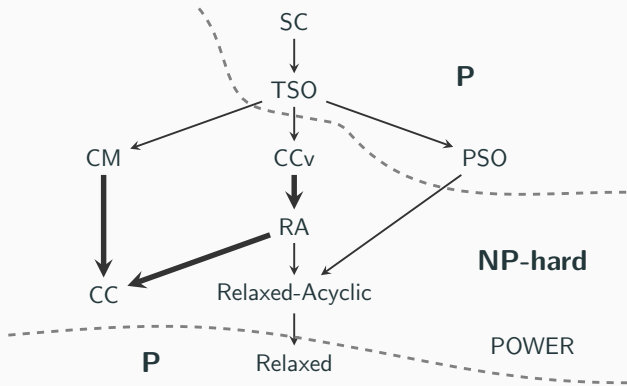
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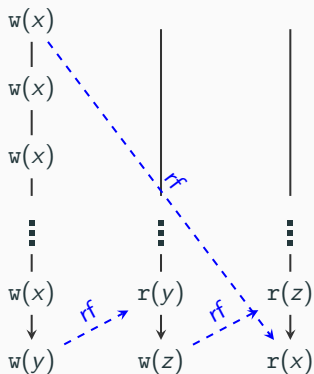
However! Bounded testing is in **P** for some weak memory

How Hard is Weak Memory Testing? Very Hard

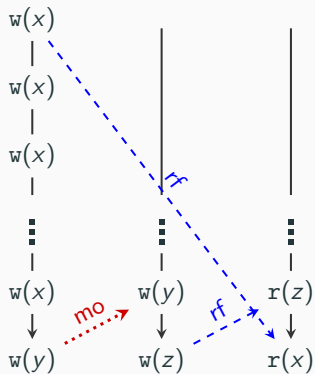


Thank you!
Questions?

Why Should this be Hard?

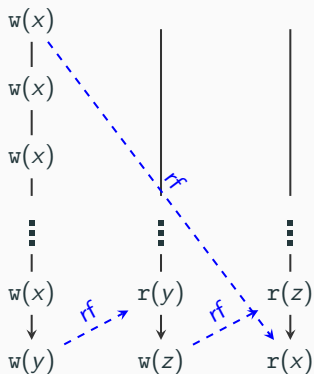


Not Causally Consistent

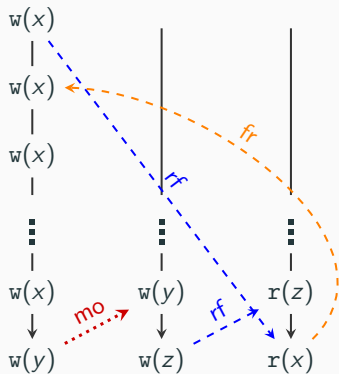


Causally Consistent

Why Should this be Hard?



Not Causally Consistent



Causally Consistent; but not SC

Reads-From (RF) Testing

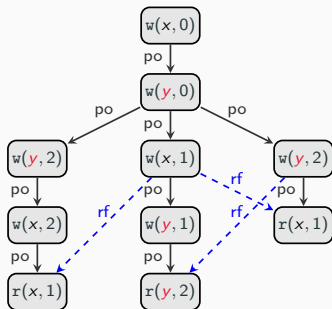
RF-Testing

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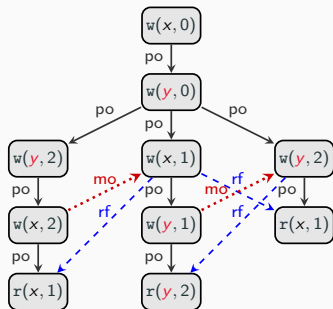
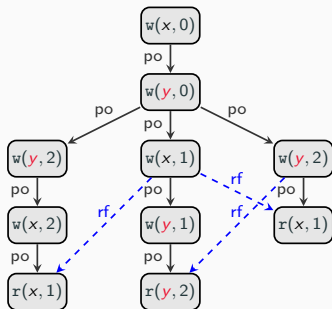
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Multi-copy atomicity?