

# A Survey on Priority Queues

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**Abstract.** Back in 1964 Williams introduced the binary heap as a basic priority queue data structure supporting the operations INSERT and EXTRACTMIN in logarithmic time. Since then numerous papers have been published on priority queues. This paper tries to list some of the directions research on priority queues has taken the last 50 years.

## 1 Introduction

In 1964 Williams introduced “Algorithm 232” [125]—a data structure later known as *binary heaps*. This data structure essentially appears in all introductory textbooks on Algorithms and Data Structures because of its simplicity and simultaneously being a powerful data structure.

A tremendous amount of research has been done within the design and analysis of priority queues over the last 50 years, building on ideas originating back to the initial work of Williams and Floyd in the early 1960s. In this paper I try to list some of this work, but it is evident by the amount of research done that the list is in no respect complete. Many papers address several aspects of priority queues. In the following only a few of these aspects are highlighted.

## 2 The beginning: Binary heaps

Williams’ binary heap is a data structure to store a dynamic set of elements from an ordered set supporting the insertion of an element (INSERT) and the deletion of the minimum element (EXTRACTMIN) in  $O(\lg n)$  time, where  $n$  is the number of elements in the priority queue<sup>1</sup>. Williams’ data structure was inspired by Floyd’s 1962 paper on sorting using a tournament tree [65], but compared to Floyd’s earlier work a binary heap is *implicit*, i.e. the data structure only uses one array of size  $n$  for storing the  $n$  elements without using any additional space<sup>2</sup>. For a set of size  $n$  it is simply an array  $A[1..n]$  storing the  $n$  elements in a permutation implicitly representing a binary tree satisfying *heap order*, i.e.  $A[i] \leq A[2i]$  and

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<sup>1</sup> We let  $\lg x$  denote the binary logarithm of  $x$ .

<sup>2</sup> In the following we denote a data structure storing  $O(1)$  words of  $\lg n$  bits between the operations also as being implicit. The additional space will be stated explicitly in these cases.

$A[i] \leq A[2i+1]$  for all  $1 \leq i \leq n$  (provided  $2i \leq n$  and  $2i+1 \leq n$ , respectively). Williams gave an algorithm for constructing a heap from an array of  $n$  elements in  $O(n \lg n)$  time [125]. This was subsequently improved by Floyd [66] to  $O(n)$ .

The average case performance of the operations on a binary heap was studied in a sequence of papers. Porter and Simon [111] introduced the random heap model, and proved that random insertions into a random heap require about 1.61 element exchanges. Bollobás and Simon [10] considered inserting a random sequence of elements into an initially empty binary heap and proved that the average number of exchanges per element inserted is about 1.7645. Doberkat studied the average number of exchanges for EXTRACTMIN in a random heap [38] and for Floyd’s heap construction algorithm [39].

Gonnet and Munro considered the constants in the number of comparisons to maintain a binary heap [80], and gave upper and lower bounds proving that INSERT requires  $\lg \lg n \pm O(1)$  comparisons (the upper bound still requiring  $O(\lg n)$  elements to be moved) and EXTRACTMIN requires  $\lg n + \lg^* n \pm O(1)$  comparisons. An  $1.5n - O(\lg n)$  lower bound for the number of comparisons for constructing a heap was proved by Carlsson and Chen in [22].

Sack and Strothotte [113] showed how to support the merging of two binary heaps (MELD) of size  $n$  and  $k$  in time  $O(k + \lg n \cdot \lg k)$ , where  $k \leq n$ . If the heaps are represented as binary trees using pointers the additive “ $k$ ” term disappears from their bound.

It is obvious that the  $k$  smallest elements in a heap can be extracted by  $k$  applications of EXTRACTMIN in  $O(k \lg n)$  time. Frederickson [73] proved that the partial order given by a binary heap (or more generally, from any heap ordered binary tree) allows us to select the  $k$  smallest elements in  $O(k)$  time (reported in arbitrary order).

### 3 Reducing the number of comparisons

All the results in Section 2 assume the partial order of the original binary heaps of Williams. In this section we summarize work on lowering the constants in the number of comparisons by considering priority queues with alternative requirements with respect to the order maintained.

Johnson [88] generalized binary heaps to implicit  $d$ -ary heaps with  $O(\lg_d n)$  and  $O(d \lg_d n)$  time for INSERT and EXTRACTMIN, respectively. By setting  $d = \omega(1)$ ,  $d$ -ary heaps achieve sublogarithmic insertion time. Subsequently many priority queues achieved constant time INSERT and logarithmic time EXTRACTMIN, surpassing the bounds of Johnson.

The weak heaps of Peterson and Dutton [108, 41] are not completely implicit like binary heaps. They store the input elements in one array and require one additional bit per element. Edelkamp and Wegener [47] proved that sorting  $n$  elements using a weak heap uses  $n \lg n + 0.09n$  comparisons, getting very close to the information theoretic lower bound of  $n \lg n - 1.44n$  comparisons [67]. A refinement of weak heap sorting performs at most  $n \lg n - 0.9n$  comparisons [46].

Edelkamp *et al.* [43] studied variations of weak heaps, in particular they reduced the cost of INSERT to be constant.

Elmasry *et al.* [56] studied how to reduce the number of comparisons for priority queues to get close to the optimal number of comparisons, and presented a pointer based spriority queue implementation supporting INSERT with  $O(1)$  comparisons and EXTRACTMIN with  $\lg n + O(1)$  comparisons. Edelkamp *et al.* [45] recently achieved the same bounds by an implicit priority queue using  $O(1)$  extra words of  $\lg n$  bits.

## 4 Double-ended priority queues

Atkinson *et al.* [8] generalized the partial order of binary heaps and introduced the implicit Min-Max heaps supporting both EXTRACTMIN and EXTRACTMAX in logarithmic time and having linear construction time. Essentially Min-Max heaps and all the following double-ended priority queues maintain a Min-heap and a Max-heap for some partition of the elements stored.

Carlsson introduced the implicit Deap [21] as an alternative to Min-Max heaps, improving the number of comparisons for EXTRACTMIN/EXTRACTMAX from  $\frac{3}{2} \lg n + \lg \lg n$  to  $\lg n + \lg \lg n$ . Carlsson *et al.* [23] gave a proof that a Deap can be constructed in linear time.

General techniques to convert single ended priority queues into double-ended priority queues were presented by Chong and Sahni [32] and Elmasry *et al.* [57]. Alternative implementations of implicit double-ended queues include [106, 26, 37, 99, 6]. Ding and Weiss [35] presented an implicit double-ended priority queue for multi-dimensional data.

Double-ended priority queues supporting MELD were presented by Ding and Weiss [36], Khoong and Leong [96], and Cho and Sahni [31], which are based on min-max heaps, binomial queues, and leftist trees, respectively.

## 5 Implicit data structures

The original implicit heaps of Williams require  $O(\lg n)$  worst-case time for INSERT and EXTRACTMIN. Similar bounds are achieved by several of the above mentioned implicit double-ended priority queues. Carlsson *et al.* [24] described an implicit heap with  $O(1)$  time INSERT and  $O(\lg n)$  time EXTRACTMIN, storing  $O(1)$  extra words of  $\lg n$  bits between operations. Edelkamp *et al.* [45] presented an implicit priority queue with the same time bounds and also using  $O(1)$  extra words, but only requiring  $\lg n + O(1)$  comparisons per EXTRACTMIN. A priority queue with amortized  $O(1)$  time INSERT and  $O(\lg n)$  time EXTRACTMIN was presented by Harvey and Zatloukal [84], that does not store any additional information between operations.

The existence of efficient implicit priority queue data structures implied the canonical question if efficient implicit dynamic dictionaries also existed. The study of implicit dynamic dictionaries was initiated by Munro and Suwanda [104]

who proved tight  $\Theta(\sqrt{n})$  bounds on implicit dictionaries satisfying a fixed partial order. The bounds for implicit dictionaries were subsequently improved by Fredrickson [71] who achieved logarithmic time searches and  $O(n\sqrt{2/\lg n} \cdot \lg^{3/2} n)$  time updates, and the first polylogarithmic bounds were given by Munro in [101] achieving  $O(\lg^2 n)$  time for both updates and searches by encoding the bits of pointers for an AVL-tree by the relative order of pairs of elements. Munro and Poblete [103] presented a semi-dynamic implicit dictionary with  $O(\lg^2 n)$  time insertions and  $O(\lg n)$  time searches. Subsequently update time was improved to  $O(\lg^2 n / \lg n)$  by implicit B-trees [69], and eventually logarithmic time bounds were obtained by Franceschini and Grossi [68]. Franceschini and Munro [70] furthermore reduced the number of exchanges to  $O(1)$  while keeping the number of comparisons per operation logarithmic (update bounds being amortized).

## 6 DecreaseKey and Meld

Dijkstra's single source shortest paths algorithm makes essential use of priority queues, and in particular the primitive of lowering the priority of an existing element in the priority queue. Fredman and Tarjan [77] introduced the DECREASEKEY operation for this and presented Fibonacci heaps, supporting DECREASEKEY in amortized constant time implying a running time of  $O(m+n \lg n)$  for Dijkstra's algorithm, improving the previous bound of  $O(m \lg_{m/n} n)$  achieved using an  $m/n$ -ary heap [89]. Fibonacci heaps also resulted in improved algorithms for computing minimum trees in weighted graphs with running time  $O(m \lg^* n)$ . Fibonacci heaps are a generalization of the binomial queues of Vuillemin [123], which achieve the same performance as Fibonacci heaps except for the DECREASEKEY operation. In particular both data structures support MELD in amortized constant time. The worst-case time for MELD in a binomial queue is  $\Theta(\lg n)$ , but the amortized time was proven to be constant by Khoong and Leong [96].

A sequence of priority queues achieves the same amortized performance as Fibonacci heaps. Peterson [108] gave a solution based on AVL-trees, Driscoll *et al.* [40] presented the rank-relaxed heaps, Kaplan and Tarjan [94] presented the thin heaps, Chan [25] presented the quake heaps, Haeupler *et al.* [81] presented the rank-pairing heaps, and Elmasry [53] presented the violation heaps. Høyer [85] presented a general technique to construct different data structures achieving time bounds matching those of Fibonacci heaps, using red-black, AVL-trees and  $(a, b)$ -trees. Elmasry improved the number of comparisons of Fibonacci heaps by a constant factor [48].

A direction of research has been to develop priority queues with worst-case time guarantees for the operations supported by Fibonacci heaps. The run-relaxed heaps by Driscoll *et al.* [40] achieve worst-case constant time DECREASEKEY operations, but MELD takes logarithmic time. The same result was achieved by Kaplan and Tarjan [93] with fat heaps. Elmasry *et al.* presented two-tier relaxed heaps [58] in which the number of comparisons for EXTRACTMIN is reduced to  $\lg n + 3 \lg \lg n + O(1)$ . Elmasry *et al.* [55] achieve similar bounds where

DECREASEKEY operations are supported with  $\lg n + O(\lg \lg n)$  comparisons by introducing structural violations instead of heap order violations. The first priority queue with worst-case  $o(\lg n)$  time MELD was a generalization of binomial queues by Fagerberg [63], supporting MELD in  $o(\lg n)$  time and EXTRACTMIN in time  $\omega(\lg n)$ . A priority queue with constant time INSERT and MELD, and logarithmic time EXTRACTMIN and DECREASEKEY was presented by Brodal [11]. A purely functional implementation of [11] (without DECREASEKEY) was given by Brodal and Okasaki in [17].

Comparison based priority queues with worst-case constant time INSERT, DECREASEKEY and MELD and logarithmic time EXTRACTMIN were presented by Brodal [12], assuming the RAM model. Similar bounds in the RAM model were achieved by Elmasry and Katajainen [59]. Brodal *et al.* [16] recently achieved matching bounds in the pointer machine model

Common to many of the priority queues achieving good worst-case bounds for MELD and/or DECREASEKEY are that they use some redundant counting scheme [33] to control the number of trees in a forest of heap ordered trees, the number of structural violations and/or heap order violations.

Kaplan *et al.* [92] emphasized the requirement that the DECREASEKEY operation as arguments must take both the element to be deleted and a reference to the priority queue containing this element, since otherwise FINDMIN, DECREASEKEY, or MELD must take non-constant time.

Chazelle [28] introduced the soft heap, a priority queue specialized toward minimum spanning tree computations that is allowed to perform a limited number of internal errors. A simplified version of soft heaps was given by Kaplan and Zwick [95]. Minimum spanning tree algorithms using soft heaps were presented by Chazelle [27] and Pettie and Ramachandran [110], where [110] is an optimal minimum spanning tree algorithm but with unknown complexity.

Mortensen and Pettie [43] presented an implicit priority queue supporting INSERT and DECREASEKEY in amortized constant time and EXTRACTMIN in logarithmic time, using  $O(1)$  words of extra storage.

## 7 Self-adjusting priority queues

Crane [34] introduced the leftist heaps. The leftist heaps of Crane are height balanced heap ordered binary trees, where for each node the height of the left subtree is at least the height of the right subtree. Cho and Sahni [30] introduced a weight-balanced version of leftist trees. Okasaki [105] introduced maxiphobic heaps as a very pedagogical and easy to understand priority queue where operations are based on the recursive melding of binary heap ordered trees.

Sleator and Tarjan introduced the skew heaps [117] as a self-adjusting version of leftist heaps [34], i.e. a data structure where no balancing information is stored at the nodes of the structure and where the structure is adjusted on each update according to some local updating rule. A tight analysis was given in [91, 115] for the amortized number of comparisons performed by EXTRACTMIN and MELD in a skew heap, showing that the amortized number of comparisons

is approximately  $\lg_\phi n$ , where  $\phi = (\sqrt{5} + 1)/2$  is the golden ratio. The upper bound was given by Kaldewaij and Schoenmakers [91] and the matching lower bound was given by Schoenmakers [115].

Pairing heaps [76] were introduced as a self-adjusting version of Fibonacci heaps, but the exact asymptotic amortized complexity of pairing heaps remains unsettled. Stasko and Vitter [118] did an early experimental evaluation showing that DECREASEKEY was virtually constant. Fredman later disproved this by showing a lower bound of amortized time  $\Omega(\lg \lg n)$  for the DECREASEKEY operation on pairing heaps [74]. Iacono [86] gave an analysis of pairing heaps achieving amortized constant INSERT and MELD, and logarithmic EXTRACTMIN and DECREASEKEY operations. Pettie [109] proved an upper bound of amortized  $O(2^{2\sqrt{\lg \lg n}})$  time for INSERT, MELD and DECREASEKEY, and amortized  $O(\lg n)$  time for EXTRACTMIN.

Variations of pairing heaps were considered in [118, 51, 52], all achieving amortized constant time INSERT. Stasko and Vitter [118] achieved that MELD, DECREASEKEY, and EXTRACTMIN all take amortized  $O(\lg n)$  time. Elmasry in [49] examined parameterized versions of skew heaps, pairing heaps, and skew-pairing heaps, both theoretically and experimentally, and in [51] and [52] showed how to improve the time bound for DECREASEKEY to amortized  $O(\lg \lg n)$  and the time bounds for MELD to amortized  $O(\lg \lg n)$  and amortized  $O(1)$ , respectively.

## 8 Distribution sensitive priority queues

Priority queues with distribution-sensitive performance have been designed and analyzed (similarly to the working-set properties of splay trees for dictionaries [116]). Fischer and Paterson’s fishspear priority queue [64] supports a sequence of INSERT and EXTRACTMIN operations, where the amortized cost for handling an element is logarithmic in the “max-depth” of the element, i.e. over time the largest number elements less than the element simultaneously in the priority queue. Iacono [86] proved that for pairing heaps EXTRACTMIN on an element takes amortized logarithmic time in the number of operations performed since the insertion of the element. The funnel-heap of Brodal and Fagerberg [13] achieves EXTRACTMIN logarithmic in the number of insertions performed since the element to be deleted was inserted. Elmasry [50] described a priority queue where EXTRACTMIN takes time logarithmic in the number of elements inserted after the element to be deleted was inserted and are still present in the priority queue. Iacono and Langerman [87] introduced the Queap priority queue where EXTRACTMIN takes time logarithmic in the number of elements inserted *before* the element to be deleted and still present in the priority queue, a property denoted “queueish”. Elmasry *et al.* [54] describe a priority queue with a unified property covering both queueish and working set properties.

## 9 RAM priority queues

Priority queues storing non-negative integers and where the running time depends on the maximal possible value  $N$  stored in the priority queue were presented by van Emde Boas *et al.* [60, 61], who achieved INSERT and EXTRACTMIN in time  $O(\lg \lg N)$  using space  $O(N \lg \lg N)$  and  $O(N)$  in [61] and [60], respectively. Using dynamic perfect hashing, the Y-fast tries of Willard [124] reduces the space to  $O(n)$ , by making the time bounds amortized randomized  $O(\lg \lg N)$ .

Subsequent work initiated by the fusion trees of Fredman and Willard [78] has explored the power of the RAM model to develop algorithms with  $o(\lg n)$  time priority queue operations and being independent of the word size  $w$  (assuming that elements stored are integers in the range  $\{0, 1, \dots, 2^w - 1\}$ ). Fusion trees achieve  $O(\lg n / \lg \lg n)$  time INSERT and EXTRACTMIN using linear space

Thorup [120] showed how to support INSERT and EXTRACTMIN in  $O(\lg \lg n)$  time for  $w$ -bit integers on a RAM with word size  $w$  bits (using superlinear space or linear space using hashing). Linear space deterministic solutions using  $O((\lg \lg n)^2)$  amortized and worst-case time were presented by Thorup [119] and Andersson and Thorup [3], respectively. Raman [112] presented a RAM priority queue supporting DECREASEKEY, resulting in an  $O(m + n\sqrt{\lg n \lg \lg n})$  time implementation of Dijkstra's single source shortest path algorithm.

That priority queues can be used to sort  $n$  numbers is trivial. Thorup in [122] proved that the opposite direction also applies: Given a RAM algorithm that sorts  $n$  words in  $O(n \cdot S(n))$  time, Thorup describes how to support INSERT and EXTRACTMIN in  $O(S(n))$  time, i.e. proving the equivalence between sorting and priority queues. Using previous deterministic  $O(n \lg \lg n)$  time and expected  $O(n\sqrt{\lg \lg n})$  time RAM sorting algorithms by Han [82] and Han and Thorup [83], respectively, this implies deterministic and randomized priority queues with INSERT and EXTRACTMIN in  $O(\lg \lg n)$  and expected  $O(\sqrt{\lg \lg n})$  time, respectively. Thorup [121] presented a RAM priority queue supporting INSERT and DECREASEKEY in constant time and EXTRACTMIN in  $O(\lg \lg n)$  time, resulting in an  $O(m + n \lg \lg n)$  time implementation of Dijkstra's single source shortest path algorithm.

A general technique to convert non-meldable priority queues with INSERT operations taking more than constant time to a corresponding data structure with constant time INSERT operations was presented by Alstrup *et al.* [2]. A general technique was described by Mendelson *et al.* [100] to convert non-meldable priority queues without DECREASEKEY into a priority queue supporting MELD in constant time and with an additive  $\alpha(n)$  cost in the time for the DELETE operation, i.e. the operation of deleting an arbitrary element given by a reference. Here  $\alpha$  is the inverse of the Ackermann function.

Brodnik *et al.* studied the power of the RAMBO model (random access machine with byte overlap). In [18] they showed how to support INSERT and EXTRACTMIN in constant time (and in [19] they showed how to perform constant time queries and updates for the dynamic prefix sum problem).

## 10 Hierarchical memory models

Early work on algorithm design in the 60s and 70s made the (by then realistic) assumption that running time was bound by the number of instructions performed, and the goal was to construct algorithms minimizing the number of instructions performed. On modern computer architectures the running time of an algorithm implementation is often not dominated by the number of instructions performed, but by other factors such as the number of cache faults, page faults, TLB misses, and branch mispredictions. This has led to computational models such as the I/O-model of Aggarwal and Vitter [1] and the cache-oblivious model Frigo *et al.* [79], modeling that the bottleneck in a computation is the number of cache-line or disk-block transfers performed by an algorithm. The I/O-model assumes that the parameters  $M$  and  $B$  are known to the algorithm, where  $M$  and  $B$  are the capacity in elements of the memory and a disk block, respectively. In the cache-oblivious model the block and memory parameters are not known by the algorithm, with the consequence that a cache-oblivious algorithm with good I/O-performance automatically achieves good I/O-performance on several levels.

Fadel *et al.* [62] described an amortized I/O-optimal priority queue by adopting binary heaps to external memory by letting each node store  $\Theta(M)$  elements and the degree of each node be  $\Theta(M/B)$ . An alternative solution with the same amortized performance was achieved by Arge [4] using a “buffer tree”. An external memory priority queue with worst-case bounds matching the previous structures amortized bounds was presented in [15].

Cache-oblivious priority queues were presented by Arge *et al.* [5] and Brodal and Fagerberg [13]. External memory and cache oblivious priority queues supporting an adapted version of DECREASEKEY to solve the single source shortest path problem on undirected graphs with  $O(n + \frac{m}{B} \lg \frac{m}{B})$  I/Os were presented by Kumar and Swabe [97] and Brodal *et al.* [14], respectively.

Fischer and Paterson [64] introduced the Fishspear priority queue designed for sequential storage such as a constant number of stacks or tapes, and using amortized  $O(\lg n)$  time per INSERT and EXTRACTMIN operation.

## 11 Priority queues for sorting with limited space

Since the seminal paper by Munro and Patterson [102] on sorting and selection for read-only input memory with a limited read-write working space (and write-only output memory for the case of sorting), a sequence of papers have presented priority queues for sorting in this model. Frederickson [72] achieved a time-space product of  $O(n^2 \lg n)$  for sorting, and [107] and [7] achieved an  $O(n^2)$  time-space product for a wide-range of working space sizes, which was proven to be optimal by Beame [9].



## 12 Empirical investigations

Many experimental evaluations of priority queues have been done, e.g. [20, 90, 98, 30, 75, 29, 44]. The importance of cache misses were observed in [98], and an implementation adopted to be cache efficient based on merging sorted lists and making efficient use of registers was presented by Sanders [114].

Modern machines are complex and an efficient implementation is not necessarily an implementation performing the fewest possible instructions. As mentioned, other parameters that are important to reduce are e.g. the number of cache misses, number of TLB misses, number of branch mispredictions, and number of branches performed. Memory hierarchy issues can be addressed on the algorithm design level, other issues such as branch mispredictions can be reduced by using special CPU instructions such as predicated instructions such as conditional move instruction (e.g. the CMOV instruction available on the Intel Pentium II and later processors), and exploiting parallelism using e.g. SIMD instructions. Some recent work considering priority queues in this context was done by Edelkamp *et al.* [42].

## 13 Concluding remarks

As stated in the introduction, this paper lists some of the research done related to priority queues, but the list is not expected to be complete. A lot of branches of related work have not been discussed. Examples are: Work on discrete event simulation that makes heavy use of priority queues, and where a lot of work on specialized priority queues has been done; priority queues in parallel models, both practical and theoretical work; concurrency issues for parallel access to a priority queue; results on sorting based on priority queues; just to mention few.

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