

Automated Verification of Parametric Channel-Based Process Communication

[GEORGIAN-VLAD SAIOC](HTTPS://ORCID.ORG/0009-0000-1714-3866)[∗] , Aarhus University, Denmark [JULIEN LANGE,](HTTPS://ORCID.ORG/0000-0001-9697-1378) Royal Holloway, University of London, United Kingdom [ANDERS MØLLER,](HTTPS://ORCID.ORG/0000-0003-1333-2314) Aarhus University, Denmark

A challenge of writing concurrent message passing programs is ensuring the absence of partial deadlocks, which can cause severe memory leaks in long running systems. Several static analysis techniques have been proposed for automatically detecting partial deadlocks in Go programs. For a large enterprise code base, we found these tools too imprecise to reason about process communication that is parametric, i.e., where the number of channel communication operations or the channel capacities are determined at runtime.

We present a novel approach to automatically verify the absence of partial deadlocks in Go program fragments with such parametric process communication. The key idea is to translate Go fragments to a core language that is sufficiently expressive to represent real-world parametric communication patterns and can be encoded into Dafny programs annotated with postconditions enforcing partial deadlock freedom. In situations where a fragment is partial deadlock free only when the concurrency parameters satisfy certain conditions, a suitable precondition can often be inferred.

Experimental results on a real-world code base containing 583 program fragments that are beyond the reach of existing techniques have shown that the approach can verify the absence of partial deadlocks in 145 cases. For an additional 228 cases, a nontrivial precondition is inferred that the surrounding code must satisfy to ensure partial deadlock freedom.

CCS Concepts:• Software and its engineering→Formal software verification;• Theory of computation \rightarrow Automated reasoning.

Additional Key Words and Phrases: Go, message passing concurrency, partial deadlocks, static analysis, automated verification, invariant discovery

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1 Introduction

Message passing concurrency in the style of Hoare's CSP [\[13\]](#page-24-0) has experienced a resurgence through the Go programming language [\[37\]](#page-25-0), especially for systems development. Go prominently treats concurrency as a first-class citizen, with its forefront feature being the forking of threads, colloquially known as goroutines, simply by prefixing function calls with the **go** keyword. Goroutines may communicate via shared memory or channel-based message passing, where the latter paradigm is favored by the language designers [\[36\]](#page-25-1). As with locks in shared memory concurrency, channel

[∗]Also with Uber Technologies, Inc.

Authors' Contact Information: [Georgian-Vlad Saioc,](https://orcid.org/0009-0000-1714-3866) Aarhus University, Aarhus, Denmark, gvsaioc@cs.au.dk; [Julien Lange,](https://orcid.org/0000-0001-9697-1378) Royal Holloway, University of London, Egham, United Kingdom, Julien.Lange@rhul.ac.uk; [Anders Møller,](https://orcid.org/0000-0003-1333-2314) Aarhus University, Aarhus, Denmark, amoeller@cs.au.dk.

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communication operations may block (e.g., attempting to read from an empty channel). Because of the complex program structure that often exists in multi-threaded code, programming errors sometimes lead to a process being permanently stuck on a channel operation, which is referred to as a partial deadlock, also known as a goroutine leak or blocking error. Partial deadlocks not only cause possibly important actions to be missed; they also often result in severe memory leaks because the involved goroutines are never garbage collected by the Go runtime system [\[30,](#page-25-2) [31\]](#page-25-3).

In many cases, communication is localized to a small set of related functions (a *program fragment*), and depends on a set of values (*concurrency parameters*) that dictate the number of communication operations or limit the sizes of channel message queues. The code surrounding the fragment may enforce certain properties on the concurrency parameters which rule out partial deadlocks. The state-of-the-art tools for detecting partial deadlocks in Go are fundamentally unable to reason about parametric fragments i.e., they cannot distinguish erroneous fragments from correct ones, and the extensions needed to overcome these shortcomings are not obvious. GCatch [\[22\]](#page-24-1) ignores channels with capacities that are not fixed statically and only analyzes loops up to two iterations. Goat [\[38\]](#page-25-4) has limited support for loops and conservatively over-approximates channel capacities without relating them to other program variables. Gomela [\[8\]](#page-24-2) tests a finite number of execution paths by instantiating concurrency parameter. We cover related work in more detail in Section [8.](#page-22-0)

We propose a sound approach to automatically verify the absence of partial deadlocks in Go program fragments that involve parametric process communication. We have designed a core language, VirGo, that is sufficiently expressive to model many parametric fragments, and built the tool Ginger that encodes VirGo programs into Dafny [\[19\]](#page-24-3) and suggests preconditions over concurrency parameters guaranteeing partial deadlock freedom. Encoded fragments are subsequently verified in Dafny relative to the suggested preconditions. The verification procedure is often able to confirm that the suggested precondition is the weakest precondition that ensures partial deadlock freedom in the original program fragment. The precondition is often trivial (i.e., 'true'), meaning that the fragment is unconditionally free of partial deadlocks. Whenever the precondition is nontrivial, it can be used for fragment documentation or as requirements that the code surrounding the fragment must satisfy to avoid errors.

The approach is intended to complement existing techniques (which cover many advanced Go features) when fragments rely on parametric communication. Although we focus on Go in this paper, the core of the proposed approach is applicable to other programming languages that support message-passing programming. It is specifically relevant to languages that offer buffered channels with bounded capacity (e.g., Rust and Kotlin).

To summarize, we make the following contributions:

- (1) We describe the VirGo language for modeling a family of Go program fragments that implement parametric communication, which other analysis tools cannot reason about.
- (2) We show how to automatically verify partial deadlock freedom in VirGo programs by translating them into Dafny. In addition, we present a mechanism for suggesting preconditions, which can be incorporated into the verification procedure to reason about program fragments that are only conditionally correct. We aim for a sound approach: when a VirGo program is deemed free of partial deadlock (possibly conditionally, as expressed by the inferred precondition), then so is the original Go code.
- (3) The approach has been implemented in a tool named Ginger, which automatically verifies Go code and suggests preconditions. We evaluate GINGER on a large industrial code base at Uber Technologies, Inc. containing 583 program fragments with parametric channel-based process communication, all of which are beyond the reach of existing Go analysis tools. The

```
1 func GetResult(x int) {
2 c := make ( chan T )
3 // Send x messages
4 for i := 0; i < x ; i ++ {
5 go func () {
6 // Supply result to c
7 c <- work ()
8 }
9 }
10 // Retrieve one result
11 \cdots <-c
12 \sqrt{3}func GetResult(x int) {
                                  c := make ( chan T, x)
                                  for i := 0; i < x; i++go func () {
                                     c <- work ()
                                    }
                                   }
                                   \ldots <-c
                                 \mathbf{A}func GetResult(x int) {
                                                                 if x \le 0 {
                                                                  return // or panic/log
                                                                   }
                                                                  c := make ( chan T , x )
                                                                 for i := 0; i < x; i++)go func () {
                                                                      c <- work ()
                                                                    }
                                                                  }
                                                                  \ldots <- c
                                                                 }
```
Fig. 1. Go fragments with parametric communication. Left: Original buggy fragment. Middle: Fragment after fix; channel capacity guarantees all senders unblock. Right: Partial deadlock-free fragment (for all inputs).

experimental results show that the technique can verify the absence of partial deadlocks in 373 fragments, 228 thereof with a nontrivial precondition.

2 Motivating Example

Go is an imperative, concurrent programming language with support for channel-based communication. A channel is a bounded message queue with a capacity specified when the channel is created. A goroutine blocks if attempting to send to a full channel or receive from an empty channel until another goroutine has performed the dual operation. Channels support either synchronous (default) or asynchronous communication (when the capacity is strictly positive). The standard library sync provides other concurrency primitives, the most prominent being Mutex (standard locks) and WaitGroup (barriers managed by a counter). Go also has **select** statements, allowing a goroutine to wait on multiple channel operations and non-deterministically choose one once it becomes enabled, and channel **close**, which unblocks all receive operations.

Alongside concurrency primitives, Go supports standard imperative features, including conditional statements and loops. It is common to combine these features and channel-based concurrency to implement powerful patterns like the fork-join model [\[7\]](#page-24-4). The Go runtime signals global deadlocks by halting the entire system, but partial deadlocks remain invisible. Furthermore, global deadlocks are rare in practice, as real-world systems often involve several independent sets of related goroutines (service instances implemented in our benchmark predominantly feature over 2, 000 concurrent goroutines), where only some may deadlock. Partial deadlocks, on the other hand, are common in practice, and generally more difficult to diagnose, with an impact that is not immediately observable. In fact, they are often undiagnosed for long periods of time, surreptitiously impacting overall memory efficiency and wasting server resources, and, in the worst cases, leading to out-of-memory exceptions and system failures [\[30,](#page-25-2) [31\]](#page-25-3).

In typical real-world Go programs, it is possible to identify fragments, each consisting of a set of related functions that create and use a number of goroutines and channels to collectively achieve a specific purpose. Several existing Go analysis tools, including GCatch [\[22\]](#page-24-1), Goat [\[38\]](#page-25-4) and Gomela [\[8\]](#page-24-2) decompose large programs by identifying such fragments and analyzing them one by one. This work builds on this idea to propose a novel approach to reason specifically about fragments where concurrency parameters [\[8\]](#page-24-2) are statically unknown.

Figure [1](#page-2-0) (left) shows a simplified real-life fragment encountered at Uber that implements parametric communication in the form of a single Go function, GetResult, that is intended to start a number of worker processes and await the first result. In this fragment, the integer x is a concurrency parameter that dictates the number of executed send operations. Following the instantiation of

 $a ::=$ **skip** $|c! | c? | a; a | w$. Add (e) , where $c \in \mathbb{C}$, $w \in \mathbb{W}$ $V ::= a | c = \text{chan} [e] | V; V | \text{go} { V }$ | return | $w = \text{WaitGroup} | w \dots \text{Wait}()$ $|$ **for** e .. $e \{ a \} |$ **if** $e \{ V \}$ **else** $\{ V \}$ $e ::= x | v | !e | e \oplus e$ where $\oplus \in \{+, -, *, <, ==, \land\}$ and $x \in \mathbb{X}$ $v ::=$ **true** | **false** | *n*, where $n \in \mathbb{Z}$

Fig. 2. Syntax of VirGo. For the sake of presentation, the formal treatment of the shadowed constructs is omitted from Section [4](#page-6-0) but discussed in Section [5.](#page-12-0)

channel c (line [2\)](#page-2-1), x goroutines are spawned in a loop (line [5\)](#page-2-2), each performing one send operation (line [7\)](#page-2-3). Conversely, the parent executes one receive operation on c (line [11\)](#page-2-4). Since the channel is unbuffered (the channel is created with the default capacity zero), only one send operation will unblock by synchronizing with the parent (line [11\)](#page-2-4). As a result, partial deadlocks may form at any sender beyond the first (if $x > 1$), or at the receiver if no sender is created (if $x \le 0$). The scope of c is restricted to GetResult, so in the case of a partial deadlock, blocked processes would hold a reference to c indefinitely, preventing garbage collection of both the channel and blocked goroutines. Our approach can automatically identify that precondition $x = 1$ is the weakest precondition that satisfies partial deadlock freedom. Such a strict constraint over x immediately reveals a likely unintended behavior. Identifying this early would have avoided the deployment of buggy code that caused memory leaks in affected services for a year until eventually discovered via other means [\[30\]](#page-25-2).

Upon its discovery, the bug was patched by a programmer as shown in Figure [1](#page-2-0) (middle), by supplying c with capacity x, such that all senders may unblock. Our technique automatically determines that the weakest precondition is $x > 0$, which significantly relaxes the constraints. A partial deadlock remains whenever $x = 0$, and a channel capacity error is triggered whenever $x < 0$. A design decision can be made as to whether callers of GetResult must satisfy this constraint at runtime, or if GetResult must internally ensure that no valuations of x violate correctness, as in Figure [1](#page-2-0) (right). Regardless, developer awareness of this implicit constraint is beneficial.

As demonstrated by this example, the correct implementation of parametric communication relies on identifying the proper constraints over concurrency parameters to be satisfied to avoid partial deadlocks. In our approach, these constraints are presented as a precondition over concurrency parameters. When the precondition is 'true', partial deadlock freedom holds unconditionally; in other situations the programmer can use the precondition for documentation or runtime checks.

Section [3](#page-3-0) introduces the VirGo core language for expressing parametric communication. For clarity, we present the technical contributions in two parts. In the first part, Section [4](#page-6-0) presents the key ideas of the technique by explaining how to construct preconditions and encode VirGo programs in Dafny while focusing on buffered channel operations and for-loops. In the second part, Section [5](#page-12-0) gives the technical details for the rest of the features supported by VirGo. We describe how Go program fragments can be translated to VirGo in Section [6,](#page-16-0) and evaluate the approach in Section [7.](#page-20-0)

3 A Core Language for Parametric Communication

We introduce VIRGo (Verifiable Intermediate Representation for Go), a core language modeled on a subset of Go to express fragments implementing parametric communication. Its syntax is shown in Figure [2.](#page-3-1) A VirGo program is defined over a set of channels (C), waitgroups (W), and concurrency parameters (X). A program consists of statements (V), which may be a sequence of atomics (*a*), channel declarations $(c = \text{chan} [e], \text{where } e \text{ denotes the capacity})$, process termination (**return**), forking a new process (**go** { }), conditional statements (**if**), bounded loops (**for**), or waiting on a waitgroup (w.Wait()). Sequences of atomics are broken down to noops (skip), send (c!) or receive $(c?)$ channel operations (as discussed below, channel payloads are ignored), or offsetting the counter of a waitgroup (w. $Add(e)$) with the value of e. The number of iterations of a loop **for** e_1 ... e_2 {...} is bounded such that the number of iterations is $e_2 - e_1$ or 0, whichever is larger. Loop bodies can only contain sequences of atomics. Expressions (e) may be concurrency parameters $(x \in \mathbb{X})$, Boolean or integer constants (v) , negation (!e), and common binary arithmetic and Boolean operations and comparisons.

By design, the expressiveness of VirGo is limited so that verification remains tractable, while supporting many real-world programming patterns in Go. Modeling a Go fragment in VirGo involves capturing communication operations and control-flow. Note that all variables in a VirGo program as they appear in expressions, e.g., capacities or loop bounds, are concurrency parameters. VirGo is designed to over-approximate the behavior of Go programs and thus support a sound end-to-end verification. Notably, features of a Go programs that cannot be modelled directly by VirGo may be replaced by fresh concurrency parameters (e.g., data received from a channel that is later used in the condition of an if-then-else). For brevity, we omit a formal definition of the VirGo semantics as it closely mirrors the concurrent semantics of Go (see, e.g., [\[17\]](#page-24-5) for a similar formalization).

We summarize the key design choices below:

- (1) Immutable concurrency parameters: the values of concurrency parameters may not change at runtime. This is enforced syntactically as only variables in X are allowed in expressions and VirGo does not support variable assignment.
- (2) Fixed loop bounds: only finite iteration is supported and loop bounds cannot change during the execution of the program (a consequence of item [1\)](#page-4-0).
- (3) Restricted looping behavior: the bodies of loops may only contain communication actions. Branching and process spawning are syntactically disallowed. This notably rules out VirGo programs that spawn an arbitrary number of processes (we explain how to partially lift this restriction in Section [6\)](#page-16-0).
- (4) Abstract channel payloads: the payloads of messages sent over channels are not modeled, in line with many static checkers for Go [\[8,](#page-24-2) [11,](#page-24-6) [17,](#page-24-5) [18,](#page-24-7) [22,](#page-24-1) [27\]](#page-25-5).
- (5) Focus on channels and waitgroups as primitives: VirGo only supports channels and waitgroups, as they are most affected by parameterized communication. We describe how our implementation deals with mutexes in Section [6.](#page-16-0)

Note that items [2](#page-4-1) and [3](#page-4-2) above imply that under a fair process scheduler, each process of a VirGo program eventually successfully terminates or becomes permanently blocked on a communication action. The approach does not have general support for **close** operations and **select** statements. While the former may be supported at the cost of some engineering work, the latter poses a particular problem when they occur in loops as we cannot reasonably predict what branch of the select will fire at each iteration. In fact, non-deterministic **select** statements in loops generally lead to potential deadlocks when used with parameterized channel capacity, and non-parameterized versions are supported by complementary approaches [\[8,](#page-24-2) [38\]](#page-25-4). We describe in Section [6](#page-16-0) how more features of the language (e.g., function calls and limited forms of **select**) are supported.

Technical overview. The crux of the approach is to use VirGo as an intermediate representation between a Go program fragment and a Hoare triple describing the conditions under which the fragment is free of partial deadlock. Given a VIRGo program V, we generate a triple $\{P\}$ enc(V) $\{Q\}$, where P is a precondition synthesized according to different strategies, $enc(V)$ is a Dafny program

```
\{ x = 1 \}c = chan [0];
go {
 for 0 .. x { c ! }
};
c ?
                               \{1 \leq x\}c = \text{chan} [\mathbf{x}];go {
                                for 0 .. x { c ! }
                               };
                               c ?
                                                               { true }
                                                               if x <= 0 { return } else { skip };
                                                              c = chan [x];go {
                                                               for 0 .. x { c ! }
                                                               };
                                                               c ?
```
Fig. 3. VirGo translations of the program fragments from Figure [1,](#page-2-0) annotated with synthesized preconditions (first line).

(incl. invariants) that models the execution of the constituent sequential processes of V interleaved according to an abstract scheduler, and O is a postcondition describing the termination of all processes. When Dafny determines that ${P}enc(V){Q}$ is valid, this implies that V, and its Go counterpart, are free of partial deadlock whenever the precondition P is satisfied.

A Go fragment is identified wrt. the scope of its concurrency primitives (i.e., channels and waitgroups). The translation from Go to VirGo is largely straightforward but includes program transformations that help widen the applicability of the approach. Consider Figure [3](#page-5-0) which gives the translation of each Go fragment from Figure [1.](#page-2-0) Channel creation and send/receive operations are translated as expected (note that we abstract away from the values carried by c). In order to obtain a VirGo program, we transform the for-go pattern of the Go program into a go-for pattern. This is a sound transformation since we do not model message payloads and the goroutines execute only one send action. We give more details on the translation from Go to VirGo in Section [6.](#page-16-0)

The procedure from V to enc(V) is natural but quite involved as it transforms an imperative program into an automata-like representation. More details are given in Section [4.2.](#page-7-0) For Dafny to automatically verify ${P}enc(V){Q}$, we must annotate $enc(V)$ with loop invariants. We describe the invariant generation procedure in Section [4.3,](#page-11-0) which also details how we generate a postcondition that models the termination of all processes (and therefore the absence of partial deadlocks).

The ideal precondition P is the weakest constraint over the concurrency parameters that guarantees termination of all processes, i.e., it is both sufficient (ensuring termination) and necessary (a non-gracefully-terminating execution is guaranteed to exist when the precondition is not satisfied). While a generalized process for finding such a precondition is still undiscovered, a good starting point is to optimistically check that fragments are correct for all inputs, i.e., that $\{true\}enc(V){\{O\}}$ holds. However, this may fail. For example, the first program in Figure [3](#page-5-0) has partial deadlocks when $x > 1$, so we devise an additional strategy for suggesting preconditions. This strategy is based on the idea that all processes in a VirGo program terminate if the following two criteria are satisfied:

- C1 The number of receive operations must not exceed the number of send operations (all receives unblock).
- C2 The number of send operations must not exceed the sum of the capacity of the channel and the number of receive operations (all sends unblock).

In our experience, these criteria are often sufficient to generate a precondition that allows verification of partial deadlock freedom. For the examples in Figure [3,](#page-5-0) they are applied as follows:

- (a) In the left-hand-side program [C1](#page-5-1) and [C2](#page-5-2) require that we have $1 \le x$ and $x \le 1$ respectively.
- (b) For the middle program, we have $1 \le x$ by [C1](#page-5-1), and $x \le x + 1$ by [C2](#page-5-2). Thus $1 \le x$ overall.
- (c) The reasoning is similar for the right-hand-side program, but no channel operation is reachable if $x \le 0$, hence we have $x \le 0$ ∨ 1 ≤ x, a tautology.

Additionally, we propose a variant that also checks whether the suggested precondition P is the weakest. We present these strategies to discover preconditions in more detail in Section [4.4.](#page-12-1)

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 $T \ni T ::= \text{nat} | \text{int} | \text{bool} | \text{nat} \rightarrow \text{nat}$ $M ::=$ method $f(\overline{y: T})$ returns $(\overline{y: T})$ requires \overline{E} ensures \overline{E} { S } $\mathbb{S} \ni S ::= \{\}\ \big| \ S; \ S \ \big|$ var $y := E \ \big| \ y := E \ \big|$ return $\big|$ if $E \ \{ \ S \ \} \ \big|$ else $\{ \ S \ \} \big|$ $|\text{match } E \text{ { case } } n \Rightarrow S$ $|\text{while } E \text{ invariant } E \text{ { } S \text{ } }$ $E \ni E ::= e | E \implies E | E \iff E |$ forall $y :: E |$ if E then E else $E | x(E)$

Fig. 4. COREDAFNY syntax.

4 Encoding and Verifying VirGo Programs

To automate the verification while allowing the intermediate representation to be human readable, our Hoare triples take the form of Dafny programs. The encoding consists of three main steps. Step 1: we decompose a VirGo program into several sequential processes, each encoded as a counterautomaton-like model [\[25\]](#page-25-6). All possible concurrent interleavings of these processes are modeled using an abstract scheduler and a **while**-loop. Step 2: we synthesize a postcondition describing the termination of all processes, and an invariant that represents the **while**-loop. Step 3: we construct a precondition, following three possible strategies, and ask Dafny to check whether the Hoare-triple holds.

The encoding to Dafny aims at modeling precisely the original VirGo program. Hence when Dafny validates a generated Hoare triple, the partial-deadlock freedom result holds for the source VirGo program; this in turns enables the sound verification of the original Go program.

To simplify the presentation of the approach, we require in this section that all channels are asynchronous (buffer capacity >0), and the shadowed syntactical categories of Figure [2](#page-3-1) (i.e., conditional operations and waitgroups) are omitted for now. The formalization of the translation for the whole VirGo is given in Section [5.](#page-12-0)

4.1 Preliminaries: CoreDafny

Dafny is an imperative programming language that supports the formal specification of programs using pre/postcondition, assertions, and loop invariants. Dafny's ecosystem automatically (when loops are annotated with suitable invariants) checks that a program satisfies its postcondition whenever the precondition holds.

We distill the salient features of Dafny into COREDAFNY (Figure [4\)](#page-6-1), a subset of features employed in the encoding. A COREDAFNY program consists of a single method declaration M. The method signature specifies its typed parameters and named typed return variables, where types include naturals, integers, Booleans, and functions over naturals. The signature includes preconditions $(requires E)$ and *postconditions* (ensures E). Preconditions are propositions constraining the values of input parameters. Postconditions are propositions over the return variables that must be satisfied once the execution of the method terminates.

COREDAFNY statements include the empty statement $({})$, variable declarations (var $y := E$), assignments $(y := E)$, conditional statements with optional else branches, and pattern matching over integers. CoreDafny also supports **while**-loops with arbitrary guards, which may be annotated with *invariants*. CoreDAFNY expressions include the usual logical operator, unary function calls, universal quantifiers (**forall**) and conditionals. Here we assume that VirGo expressions are a subset of COREDAFNY expressions so that they do not require translation.

Figure [5](#page-7-1) shows the structure of the COREDAFNY program the encoding generates. We give more details in the remaining part of this section. The program is given in the form a template that 344:8 Georgian-Vlad Saioc, Julien Lange, and Anders Møller

```
1 method Fragment ( Sch : nat → nat , ∀ ∈ X: 
:  ) returns (∀ ∈ Π. P( ): int)
2 // Specification
3 requires forall n :: Sch(n) < \Pi4 requires 
5 ensures 
6 { // Initialisation
    var step := 0;
8 P(\pi_0) := \emptyset;9 \forall \pi \in \Pi \setminus \{\pi_0\} : P(\pi) := -1;10 \forall c \in dom(\kappa) : var c := 0;11 \forall (\_,\_,\_,\_,i,e,\_,\_) \in \mathcal{L} var i := e;12
13 // Execution via interleaving
14 while \nend{math}15 invariant inv(V) {
16 match Sch(step) {
17 \forall \pi \in \Pi : \text{case } \pi \Rightarrow \text{match } P(\pi) {
18 \forall n \in dom(\Xi[\pi]) : \text{case } n \Rightarrow \Xi[\pi][n]19 }
20 }
21 step := step + 1;
22 }
23 }
```
Fig. 5. Structure of the CoreDafny encoding for a VirGo program represented as a mapping Ξ via the translation explained in Section [4.2.](#page-7-0)

combines CoreDafny syntax (in black or purple type-writer font) with meta-syntax (highlighted in yellow). A VirGo program is translated to a CoreDafny method whose input consists of an abstract scheduler Sch and the concurrency parameters X, and its output consists of the final program counters (represented by $P(\pi)$ for each process identifier $\pi \in \Pi$, where π_0 denotes the main process).

The scheduler Sch is an uninterpreted function that decides which process should be scheduled next. The precondition P (line [4\)](#page-7-2) is generated by the different strategies presented in Section [4.4.](#page-12-1) The postcondition Q (line [5\)](#page-7-3) is generated automatically and corresponds to the termination of all processes (Section [4.3\)](#page-11-0). It essentially requires that each program counter, $P(\pi)$, has reached its termination point, $T(\pi)$, assuming process π has started.

The body of the method consists of two parts: (1) some variable initialization (lines $7-11$) which we will describe further below, and (2) a **while**-loop. The **while**-loop models all possible interleavings of the VIRGo program and executes as long as at least one process can perform an action (*enabled(V*) holds). The loop is annotated with an invariant $inv(V)$ (line [15\)](#page-7-6), described in Section [4.3.](#page-11-0) The body consists of nested match-constructs and is generated via a mapping $\Xi: \Pi \mapsto N \mapsto \mathbb{S}$ that maps each process identifier to a map from program points N to CoreDAFNY statements $\mathbb S$. We detail how Ξ is built in Section [4.2.](#page-7-0) The outer **match**-construct selects which process is executed next as determined by Sch. For each $\pi \in \Pi$ there is a case whose body contains an inner match-construct which chooses the *instruction* to be executed next. This is determined by the value of π 's program counter (i.e., variable $P(\pi)$). There is a case for each program point *n* in the encoding of process π .

4.2 Step 1: Program Point Decomposition

The first step to transform a VirGo program into CoreDafny is to decompose it into a set of sequential components, each of which becomes a counter-automaton where counters keep track of the states of processes and concurrency primitives (e.g., the number of messages in a channel).

Conventions and notations

Translation of atomic operations

skip

ATOMIC

$$
\frac{S \text{END}}{S = \text{if } c < \kappa(c) \{c \coloneqq c+1; \ P(\pi) \coloneqq n+1\}} \\
\frac{\kappa + \langle \phi : c \cdot \rangle^n \xrightarrow{\pi} \langle \phi[n \mapsto S] \rangle^{n+1}}{\kappa + \langle \phi : c \cdot \rangle^n \xrightarrow{\pi} \langle \phi[n \mapsto S] \rangle^{n+1}}
$$

$$
\frac{1}{\gamma^{n+1}} \qquad \frac{S = \text{if } c > 0 \{c = c-1; P(\pi) = n+1\}}{\kappa + \langle \phi : c \rangle^n \xrightarrow{\pi} \langle \phi[n \mapsto S] \rangle^{n+1}}
$$

A-SEQ

$$
\kappa + \langle \phi : a_1 \rangle^n \xrightarrow{\pi} \langle \phi_1 \rangle^{n_1} \qquad \kappa + \langle \phi_1 : a_2 \rangle^{n_1} \xrightarrow{\pi} \langle \phi_2 \rangle^{n_2}
$$

 $\Xi:\Pi\mapsto\Phi$ – process encoding environment $\kappa:\mathbb{C}\mapsto\mathbb{E}\quad$ channel capacity environment ⊎ disjoint union of maps $\Xi[\pi \mapsto \phi]$ short for $\Xi[\pi \mapsto \Xi(\pi) \uplus \phi]$

$$
\kappa \vdash \langle \phi : \mathsf{skip} \rangle^n \xrightarrow{\pi} \langle \phi \rangle^n
$$

Translation of structured control flow

$$
\kappa \vdash \langle \phi : a_1; a_2 \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \phi_2 \rangle^{n_2}
$$

 $\Phi \ni \phi : N \mapsto \mathbb{S}$ process encoding

receive

$$
\left\langle \kappa, \Xi : V \rangle^n \xrightarrow{\pi} \langle \kappa, \Xi \rangle^n \right.
$$

1

$$
\frac{\kappa \vdash \langle [] : a \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \phi \rangle^{n'}}{\langle \kappa, \Xi : a \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa, \Xi[\pi \mapsto \phi] \rangle^{n'}} \qquad \frac{\text{CHAN}}{\langle \kappa, \Xi : c = \text{chan } [e] \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa[c \mapsto e], \Xi[\pi \mapsto \phi] \rangle^{n+1}}
$$
\n
$$
\frac{\langle \kappa, \Xi : c = \text{chan } [e] \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa[c \mapsto e], \Xi[\pi \mapsto \phi] \rangle^{n+1}}{\langle \kappa, \Xi : \text{go } [n \mapsto P(\pi) := n+1; P(\pi') = 0] \qquad \langle \kappa, [] : V \rangle^0 \stackrel{\pi'}{\Longrightarrow} \langle \kappa', \Xi' \rangle^{n'} \qquad \pi' \text{ fresh}
$$
\n
$$
\frac{\langle \kappa, \Xi : \text{go } [V] \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa', \Xi[\pi \mapsto \phi] \uplus \Xi'[\pi' \mapsto [-1 \mapsto \{\}, n' \mapsto \{\}] \rangle^{n+1}}{\langle \kappa, \Xi : \text{go } [V] \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa', \Xi[\pi \mapsto \phi] \uplus \Xi'[\pi' \mapsto [-1 \mapsto \{\}, n' \mapsto \{\}] \rangle^{n+1}}
$$

a-seq

$$
\langle \kappa, \Xi : V_1 \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa_1, \Xi_1 \rangle^{n_1} \qquad \langle \kappa_1, \Xi_1 : V_2 \rangle^{n_1} \stackrel{\pi}{\Longrightarrow} \langle \kappa_2, \Xi_2 \rangle^{n_2}
$$

$$
\langle \kappa, \Xi : V_1; V_2 \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa_2, \Xi_2 \rangle^{n_2}
$$

for

$$
\kappa \vdash \langle [] : a \rangle^{n+1} \stackrel{\pi}{\Longrightarrow} \langle \phi \rangle^{n'} \qquad \phi' = \left[\begin{array}{ccc} n & \mapsto \text{if } i < e_2 \{ P(\pi) = n+1 \} \text{ else } \{ P(\pi) = n' + 1 \} \\ n' & \mapsto i = i+1; P(\pi) = n & \text{with } i \text{ fresh} \end{array} \right]
$$
\n
$$
\langle \kappa, \Xi : \text{for } e_1 \dots e_2 \{ a \} \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa, \Xi[\pi \mapsto \phi \uplus \phi'] \rangle^{n'+1}
$$

Translation of VirGo programs $\begin{aligned} V \implies \Xi, \kappa \end{aligned}$

Fig. 6. Translation to sequential processes.

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 $\lim_{n \to \infty} \langle \phi \rangle^n$

Concretely, each process becomes a mapping from each program point to a CoreDafny statement that models the semantics of the next instruction.

We use the following notation (see top of Figure [6\)](#page-8-0): $\pi \in \Pi$ and $n \in N$ range over finite sets of, respectively, process identifiers and program points (i.e., possible values of the program counters). The program points are represented as integers $\mathbb Z$ according to their order in the textual program code, using −1 to denote the program point for processes that have not yet been created. Process identifiers π can similarly be represented as natural numbers N so that $0 \leq \pi < |\Pi|$. P(π) refers to the name of the CoreDafny variable holding the current value of the program counter of process π , and similarly, $T(\pi)$ is the name of the COREDAFNY variable holding the process exit program point for π .

Process encoding. We formalize the translation in Figure [6](#page-8-0) using three syntax-driven inductive judgments. The first judgment handles atomic VIRGo statements: $\kappa \vdash \langle \phi : a \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \phi' \rangle^{n'}$, where environment κ binds channel names to expressions denoting their capacity, π is the identifier of the process being translated, ϕ is the current encoding of the process π (mapping program points to COREDAFNY statements), a is the statement to be translated, n is the program point immediately before a, ϕ' is the result of adding the translation of a to ϕ , and n' is the next program point. Rule send translates a send on c into an increment of variable c (capacity allowing). Rule RECEIVE translates a receive from c as a decrement of c (if $c > 0$). Rules SKIP and A-SEQ are straightforward.

Judgment $\langle \kappa, \Xi : V \rangle^n \stackrel{\pi}{\Longrightarrow} \langle \kappa, \Xi' \rangle^{n'}$ deals with the translation of general statements (V in Figure [2\)](#page-3-1). Here, Ξ and Ξ ′ are mappings from process identifiers to process encodings (input and output, respectively) and the other parameters are identical to the previous judgment. Rule atomic models atomic statements using the corresponding judgment. We use ⊎ to denote union of maps (with disjoint keys) and we write $\Xi[\pi \mapsto \phi]$ to append ϕ to the value corresponding to π in Ξ . Rule CHAN models channel creation by recording the channel capacity e in environment κ and inserting a capacity check to ensure it is a buffered channel (the program terminates if the capacity is <1).

Rule go models the spawning of a new concurrent process: it increments the program counter of the parent process (π) and sets the program counter of the child process $(\pi',$ a globally fresh identifier) to 0, thus allowing it to start its execution. The encoding of the child is extended with two additional bindings: -1 as a value indicating the process has not yet been spawned, and n' as the last program point, to mark goroutine termination. Process π' is encoded separately in environment Ξ ′ . Rules v-seq and for are straightforward.

Finally, judgment $V \implies \Xi$, κ expresses the translation of a whole VIRGo program V with main process π_0 . In rule program, *n* becomes the final (noop) instruction point of the main program.

To generate the nested **match**-constructs of Figure [5,](#page-7-1) we produce a case in the the outer **match**construct for each $\pi \in dom(\Xi)$, then we iterate over the elements of each $\Xi(\pi)$ to generate the cases of each inner **match**-construct.

Syntactic occurrences. Figure [7](#page-10-0) (top) defines some sets of syntactic occurrences that compactly gather relevant information about the representation of higher-level constructs in the process encoding. They are used to annotate the generated CoreDafny program with logical formulae. P is the set of all occurrences of a channel declaration. Each element $p \in \mathcal{P}$ is a tuple (π, n, c) where π is the process identifier declaring the channel, n is the program point of the declaration, and c is the declared channel name. O is the set of all occurrences of a channel operation in the translated program. Each element $o \in O$ is a tuple (π, n, c, d) where π is the process identifier executing that operation, n is the program point where that operation is executed, c is the channel name on which the operation is invoked, and d is the direction of the operation: send (!) or receive (?). G is the set of all occurrences of a goroutine spawning operation. Each element $q \in G$ is a

Conventions and notations

Given function $J(r)$ and set $R = \{r_1, \ldots, r_k\}$ we write $J(R)$ for $J(r_1) \wedge \ldots \wedge J(r_k)$ $\sim T(-)$ termination point

$$
\mathbb{N} \ni \mathsf{T}(\pi) \quad \text{termination point}
$$

Syntactic occurrences

Enabled and Post-condition

$$
enabled(π) = -1 < P(π) < T(π) ∧
$$

\n
$$
P(π) = n \implies (d = ! \implies c < κ(c)) ∧ (d = ? \implies c > 0)
$$

\n
$$
(\pi, n, c, d) \in O
$$

enabled(*V*) =
$$
\bigvee_{\pi \in \Pi}
$$
 enabled(π) $\xrightarrow{\qquad V \implies \Xi, \kappa \qquad \Pi = dom(\Xi)$
terminated(*V*) = $\bigwedge_{\pi \in \Pi} P(\pi) \neq -1 \implies P(\pi) = T(\pi)$

Invariant

$$
o = (\pi, n, c, d) \qquad o \notin O_{\mathcal{L}}
$$

$$
I_{com}(o) = \text{if } n < P(\pi) \text{ then } 1 \text{ else } 0
$$

$$
I_{com}(o) = 0
$$

$$
l = (\pi, n_1, n_2, i, e, e', O)
$$
\n
$$
N = \{n \in N \mid (\pi, n, c, d) \in O\} \qquad E = \sum_{n \in N} \text{if } n < P(\pi) < n_2 \text{ then } 1 \text{ else } 0
$$
\n
$$
I \text{forCom}(c, l, d) = (i - e) * |N| + E
$$
\n
$$
p = (\pi', n', c) \qquad E(d) = \sum_{l \in L} I \text{forCom}(c, l, d) + \sum_{(\pi, n, c, l) \in O} I \text{com}((\pi, n, c, d))
$$
\n
$$
I \text{chan}(p) = P(\pi') > n' \implies 0 \le c \le \kappa(c) \land c = E(l) - E(?)
$$
\n
$$
\pi = 0 \implies k = 0 \quad \pi > 0 \implies k = -1 \qquad \qquad g = (\pi, n, \pi')
$$
\n
$$
I \text{proc}(\pi) = k \le P(\pi) \le T(\pi) \qquad I \text{go}(g) = n < P(\pi) \iff P(\pi') = -1
$$
\n
$$
I = (\pi, n, n', i, e, e', O)
$$
\n
$$
\text{Then } e \le i \le e' \quad \text{then } e \le i \le e' \land (P(\pi) < n \implies i = e) \land (n < P(\pi) < n' \implies i < e') \land (n' \le P(\pi) \implies i = e')
$$
\n
$$
\text{else } i = e \land ! (n < P(\pi) < n')
$$
\n
$$
V \implies E, \kappa \qquad \Pi = \text{dom}(E) \qquad V :: P, G, L, O
$$
\n
$$
\text{inv}(V) = \text{Ichan}(P) \land \text{Iproc}(\Pi) \land \text{Icom}(O) \land \text{IforCom}(\mathbb{C} \times L \times \{!.?) \land \text{Igo}(G) \land \text{Ifor}(L)
$$

Fig. 7. Generation of termination condition, postcondition, and invariant.

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triple (π, n, π') where π is the identifier of the parent process, n is the program point where the spawning is executed, and π' is the process identifier of the newly spawned process. ${\cal L}$ is the set of all occurrences of a **for**-loop in the source program. Each element $l \in L$ is a tuple $(\pi, n, n', i, e, e', O)$ where π is the identifier of the process executing the loop, n is the loop initialization program point, n' is the termination program point, i is the name of the loop variable (in COREDAFNY), e (resp. e') is the lower (resp. upper) bound expression, and $O \subseteq O$ is the set of all channel operations in the loop body. Finally, the set $O_L \subseteq O$ contains all operations that occur in loops. Hereafter, we write $V :: \mathcal{P}, \mathcal{G}, \mathcal{L}, O$ when the encoding of program V produces occurrence set $\mathcal{P}, \mathcal{G}, \mathcal{L}$ and O. We assume these sets to be globally available.

Initialization. The initialization phase (lines $7-11$) in Figure [5](#page-7-1) sets all state variables to their initial values. The program counter of all processes is set to −1 (except for the main thread set to 0). Each channel initially holds 0 messages, and each loop variable is set to its lower bound.

Progress. The termination condition of the while-loop, enabled(V) (line [14](#page-7-7) in Figure [5\)](#page-7-1), is formally defined in Figure [7.](#page-10-0) The aim of the *enabled(V)* property is to model when the whole program can progress, i.e., at least one process is enabled. A process with identifier π is enabled (enabled(π)) if it has started and has not terminated ($-1 < P(\pi) < T(\pi)$), and any channel action it is currently due to execute $(P(\pi) = n)$ is enabled: a send operation is enabled when the number of messages in the channel has not reached its capacity, and a receive operation is enabled when the channel is not empty.

4.3 Step 2: Termination Property and Invariants

We aim to verify that a program V gracefully terminates under certain conditions. We define graceful termination as *terminated*(V), formally defined in Figure [7](#page-10-0) as: all processes that have started (P(π) \neq -1) have terminated (P(π) = T(π)).

Dafny is only able to reason about the generated programs when annotated with suitable invariants. We generate invariant properties using function $inv(V)$, invoked at line [15](#page-7-6) of Figure [5,](#page-7-1) and defined in Figure [7.](#page-10-0) It is defined as a conjunction over predicates that enforce properties for each high-level language construct, correlating program points to process counters and channel buffer values. We use the convention in Figure [7](#page-10-0) (top) to extend any function J to sets by taking the conjunction of the application of J on each element in the set.

Predicate I chan(c) states the properties related to channel c once it has been created (left-hand-side of the implication). The number of messages in the channel must not be negative and must not exceed its capacity. Additionally, the number of messages in the channel must be equal to the difference between the number of send and receive operations performed until the current execution point. To symbolically compute the number of operations we use $I_{com}(o)$ and $I_{forCom}(c, l, d)$. Function $I_{com}(o)$ deals with operations that occur outside of loops, returning 1 if the operation has executed, 0 otherwise. Function I *forCom*(c, l, d) models operations on channel c occurring in loop l. It adds the number of operations in completed iterations and the number of operations executed in the current iteration (E) .

Predicate $I_{proc}(\pi)$ states properties for each process (with identifier π) whose program counter $(P(\pi))$ should be within the expected range. Similarly, predicate $I_{g0}(q)$ states that the program counter of a spawned process ($P(\pi')$) must be set to -1 (not yet started) if its parent π' has not yet reached its spawning instruction.

Predicate $Ifor(l)$ deals with state variables associated with loops. Essentially, if the loop is executable, then the loop variable (i) must be within the bounds, and if the program counter is within the range of the loop ($n < P(\pi) < n'$), then the loop variable must be less than the upper bound (e') .

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$$
l = (\pi, n_1, n_2, i, e_1, e_2, O) \qquad N = \{n \mid (\pi, n, c, d) \in O\} \qquad e = \text{if } e_1 > e_2 \text{ then } 0 \text{ else } e_2 - e_1
$$

\n
$$
for(c, l, d) = e * |N|
$$

\n
$$
O = \{n \mid (\pi, n, c, d) \in O \setminus O_L\}
$$

\n
$$
O_{\mathcal{DS}}(c, d) = |O| + \sum_{l \in L} for(c, l, d)
$$

\n
$$
V \Longrightarrow \Xi, \kappa
$$

\n
$$
V \Longrightarrow \Xi, \kappa
$$

\n
$$
V \Longrightarrow \epsilon
$$

\n
$$
\kappa(c) > 0 \land com(c)
$$

\n
$$
c \in dom(\kappa)
$$

Fig. 8. Precondition formula.

4.4 Step 3: Strategies for Precondition Discovery

The precondition P and postcondition Q at lines [4](#page-7-2)[–5](#page-7-3) of Figure [5](#page-7-1) depend on the strategy being used. We outline three strategies below, which we use in the given order (until success or all strategies have been attempted).

Strategy 1: Attempt to prove ${\text{true}}$ $enc(V){\text{ferminated}(V)}$, i.e., that the COREDAFNY encoding of a VIRGo programs V guarantees terminated(V) without any precondition. If this succeeds, then all processes terminate for all inputs and all schedules.

Otherwise, we synthesize a formula, $balanced(V)$, defined in Figure [8,](#page-12-2) which is used in the next two strategies. For each channel, the formula enforces that its capacity is strictly positive and (corresponding to [C1](#page-5-1) and [C2](#page-5-2) in Section [3\)](#page-3-0) that the number of send (E_1) and receive (E_2) actions match, up to the capacity of the channel $(\kappa(c))$. The function that generates symbolic expressions for E_1 and E_2 works similarly to the invariant formula I *for*Com(c, l, d) in Figure [7.](#page-10-0)

Strategy 2: Check $\{\textbf{true}\}_{enc(V)}$ {balanced(V) \iff terminated(V)}, i.e., that the COREDAFNY encoding of a VIRGo program V satisfies terminated(V) if and only if balanced(V) holds. If this succeeds, then all processes terminate for all schedules and inputs for which $balanced(V)$ holds, and balanced (V) is the weakest precondition that satisfies this requirement. This follows from the fact that all concurrency parameters are immutable in VirGo. Note that this check can be applied to any precondition suggestion strategy.

Strategy 3: Attempt to prove $\{balanced(V)\}$ enc(V) $\{terminal(V)\}$, i.e., that the COREDAFNY encoding of a VIRGo program V satisfies terminated(V) when balanced(V) holds. If this succeeds, then all processes terminate for all inputs and all schedules whenever $balanced(V)$ holds (but this may not be the weakest precondition).

5 Translating All VirGo Features to CoreDafny

This section describes the rest of the translation from VirGo to CoreDafny, including all features omitted from the previous section. These detailed extensions are not essential for understanding the main ideas of the approach but are given for the sake of completeness. We keep explanations brief as the rules are mostly simple extensions of the ones in Section [4.](#page-6-0) The extensions presented here preserve the sound approach presented earlier and are reflected in our implementation.

We describe the encoding to program points of each VirGo construct (Section [5.1\)](#page-13-0), followed by invariant and precondition generation (Sections [5.2](#page-14-0) and [5.3,](#page-16-1) respectively).

Conventions

Constants for synchronous channel encoding: $RDY = 0$ SND = 1 $ACK = -1$

Atomic operations

$$
S_1 = \textbf{if } \kappa(c) > 0 \{ \textbf{if } c < \kappa(c) \{c = c+1; P(\pi) = n+2\} \quad S_2 = \textbf{if } c == \text{ACK} \{ \textbf{else} \} \quad c := \text{RDY};
$$
\n
$$
\text{else} \{ \textbf{if } c == \text{RDY} \{c = \text{SND}; P(\pi) = n+1\} \quad P(\pi) = n+2 \}
$$
\n
$$
\text{R. } \kappa \vdash \langle \phi : c! \rangle^n \xrightarrow{\pi} \langle \phi[n \mapsto S_1, n+1 \mapsto S_2] \rangle^{n+2}
$$

$$
\tt RECEIVE
$$

$$
S = \textbf{if } \kappa(c) > 0 \{ \textbf{if } c > 0 \mid c := c-1; \ P(\pi) = n+1 \}
$$
\n
$$
\} \textbf{else } \{ \textbf{if } c == \text{SND } \{c == \text{ACK}; \ P(\pi) := n+1 \}
$$
\n
$$
\frac{\}{\kappa + \langle \phi : c \rangle^n \xrightarrow{\pi} \langle \phi[n \mapsto S] \rangle^{n+1}}
$$

wg

Additional control flow constructs

1}
\n
$$
S = \textbf{if } w < -e \text{ } \{\text{ return } \};
$$
\n
$$
w = w + e; P(\pi) = n+1
$$
\n
$$
\kappa \vdash \langle \phi : w \text{.Add}(e) \rangle^n \xrightarrow{\pi} \langle \phi[n \mapsto S] \rangle^{n+1}
$$

ADD

$$
\overline{\langle \kappa, \Xi : V \rangle^n \xrightarrow{\pi} \langle \kappa, \Xi \rangle^n}
$$

 $):= n+1$

 $\lim_{n \to \infty} \langle \phi \rangle^n$

WAIT

\n
$$
\begin{aligned}\n\phi &= [n \mapsto \text{if } w == 0 \{ P(\pi) := n+1 \}] \\
\frac{\phi}{\langle \kappa, \Xi : w \cdot \text{Wait}(\lambda) \rangle^n \xrightarrow{\pi} \langle \kappa, \Xi[\pi \mapsto \phi] \rangle^{n+1}} \\
\frac{\pi}{\langle \kappa, \Xi : w \cdot \text{Wait}(\lambda) \rangle^n \xrightarrow{\pi} \langle \kappa, \Xi[\pi \mapsto \phi] \rangle^{n+1}} \\
\frac{\pi}{\langle \kappa, \Xi : \text{return} \rangle^n \xrightarrow{\pi} \langle \kappa, \Xi[\pi \mapsto \phi] \rangle^{n+1}} \\
\frac{\pi}{\langle \kappa, \Xi : \text{return} \rangle^n \xrightarrow{\pi} \langle \kappa, \Xi[\pi \mapsto \phi] \rangle^{n+1}} \\
\frac{\pi}{\langle \kappa, \Xi : V_1 \rangle^{n+1} \xrightarrow{\pi} \langle \kappa_1, \Xi_1 \rangle^{n'}} \\
\frac{\phi &= \begin{bmatrix} n & \mapsto P(\pi) := 1 + \text{if } e \text{ then } n \text{ else } n' \\ n' & \mapsto P(\pi) := n_2 \end{bmatrix}}{\langle \kappa, \Xi : \text{if } e \{ V_1 \} \text{ else } \{ V_2 \} \rangle^n \xrightarrow{\pi} \langle \kappa_2, \Xi_2[\pi \mapsto \phi] \rangle^{n_2}}\n\end{aligned}
$$

5.1 Encoding

The rules to transform a VirGo program into CoreDafny processes are given in Figure [9.](#page-13-1) Rules are added or revised to deal with synchronous communication, waitgroups, conditionals, and return statements. Rules send and receive now check whether the channel is buffered ($\kappa(c) > 0$) or synchronous. The asynchronous case is as in Section [4.](#page-6-0) In the synchronous case ($\kappa(c) = 0$), a

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Enabled

enabled (π, n, c)	if $\kappa(c) > 0$ = then $P(\pi) = n \implies c < \kappa(c)$ else $(P(\pi) = n \implies c = RDY) \wedge (P(\pi) = n + 1 \implies c = ACK)$
	enabled ₂ (π , n , c) = $P(\pi) = n \implies (\kappa(c) > 0 \implies c > 0) \land (\kappa(c) = 0 \implies c = \text{SND})$
	enabled _C (π , n, c, d) = (d =! \implies enabled ₁ (π , n, c)) \land (d =? \implies enabled ₂ (π , n, c))
	enabled _W (π, n, w) = P (π) = n \implies w = 0
enabled (π)	$= -1 < P(\pi) < T(\pi)$ \bigwedge enabled _C (π , n, c, d) \wedge \bigwedge enabled _W (π , n, w) $(\pi, n, w) \in W_w$ $(\pi, n, c, d) \in O$
enabeled(V)	$= \bigvee$ enabled(π) $\pi \in \Pi$

Fig. 10. Revised enabled predicate with support for synchronous channels and waitgroups.

channel is only available when it is $ready$ (RDY), and the first process to send a message sets the value of the channel to sending (SND). The sender is then blocked until a receiver acknowledges the synchronization by setting the channel to ACK, and continuing to the next instruction.

Waitgroup declarations do not require new program points as the corresponding Dafny variables modeling waitgroup counters are initialized to 0. Waitgroup operations are modeled via the ADD and wait rules. Adding to the waitgroup simply increments the value with the given expression e . Note that the Go waitgroup method w.Done() is translated to w.Add(-1) in VIRGo. The guard $w < -e$ checks whether the new value is negative, in which case the execution of the entire CoreDafny model is terminated immediately (without reaching its expected end state). This models the runtime error thrown when a waitgroup counters become negative in Go. Waiting on a waitgroup blocks until the counter is zero, just like in Go.

VirGo **return** statements translate to setting the process counter to its termination point. Two program points are reserved for **if** statements: one for the branching instruction that decides the continuation, and one at the end of the **true** branch that transfers control flow to the continuation, skipping the **else** branch instructions.

Progress. In Figure [10,](#page-14-1) we give a revised *enabled*(V) predicate, used to identify whether some process can make some progress. It is extended to model the blocking behavior of unbuffered channel and w . Wait() operations. For channels, an additional case is added for processes in the rendezvous state, while send or receive operation cases take the capacity into account. For waitgroups, a process is blocked on a wait operation until the waitgroup counter is 0.

5.2 Invariants with Support for Waitgroups, Conditionals, and Return

We proceed in two steps as the invariant construction that supports **if** statements need first to keep track of the conditions that make a given program point reachable in the source VirGo program.

Reachability. A reachability environment, $\psi : (\Pi \times N) \mapsto \mathbb{E}$ binds a process program point to a Boolean expression modeling the path conditions under which the program point is reachable.

 $e \vdash \langle c? \rangle_n \stackrel{\pi}{\leadsto} \langle [(\pi, n) \mapsto e] | \mathsf{false} \rangle_{n+1}$

Conventions

 $\Psi \ni \psi : (\Pi \times N) \mapsto \mathbb{E}$ Reachability environment

Reachability for atomic operations

$$
e \vdash \langle c! \rangle_n \stackrel{\pi}{\leadsto} \langle [(\pi, n) \mapsto e] | \mathsf{false} \rangle_{n+2}
$$

 $e +$

ADD

$$
e \vdash \langle w. \text{Add}(e) \rangle_n \stackrel{\pi}{\leadsto} \langle [(\pi, n) \mapsto e] | \text{false} \rangle_{n+1}
$$

receive

$$
e \vdash \langle V \rangle_n \stackrel{\pi}{\leadsto} \langle \psi \mid e \rangle_n
$$

 $e \vdash \langle s \rangle_n \stackrel{\pi}{\leadsto} \langle \psi | e \rangle_n$

Reachability for statements

⊢ ⟨ = **chan** []⟩

RETURN

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**CHAN**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**CHAN**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**CHAN**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**CHAN**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
$$
\n**WAIT**

\n
$$
\psi = [(\pi, n) \mapsto e]
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\n**WAIT**

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\psi = [(\pi, n) \mapsto e]
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\psi = [(\pi, n) \mapsto e]
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\n**WAIT**

\n $$

$$
\frac{e \vdash \langle v \rangle_0 \quad \langle v \rangle \quad \langle \psi \mid e \rangle_n}{e \vdash \langle \text{go} \{ V \} \rangle_n \stackrel{\pi}{\rightsquigarrow} \langle \psi[(\pi, n) \mapsto e] \mid \text{false} \rangle_{n+1}}
$$

$$
\frac{e \varepsilon \left(\langle V_1 \rangle_n \stackrel{\pi}{\leadsto} \langle \psi_1 | e_1 \rangle_{n_1} \quad e \wedge ! e_1 \varepsilon \langle V_2 \rangle_{n_1} \stackrel{\pi}{\leadsto} \langle \psi_2 | e_2 \rangle_{n_2}}{e \varepsilon \langle V_1; V_2 \rangle_n \stackrel{\pi}{\leadsto} \langle \psi_1 \uplus \psi_2 | e_1 \vee e_2 \rangle_{n_2}}
$$

for

$$
e \vdash \langle a \rangle_{n+1} \stackrel{\pi}{\leadsto} \langle \psi \mid \mathbf{false} \rangle_{n'}
$$

$$
e \vdash \langle \mathbf{for} \ e_1 \ \ldots \ e_2 \ \{ \ a \ \} \rangle_n \stackrel{\pi}{\leadsto} \langle \psi \, [n \mapsto e] \mid \mathbf{false} \rangle_{n'+1}
$$

if

L,

$$
\frac{e \wedge e' \vdash \langle V_1 \rangle_{n+1} \stackrel{\pi}{\leadsto} \langle \psi_1 \mid e_1 \rangle_{n_1} \qquad e \wedge \exists e' \vdash \langle V_2 \rangle_{n_1+1} \stackrel{\pi}{\leadsto} \langle \psi_2 \mid e_2 \rangle_{n_2}}{e \vdash \langle \textbf{if } e' \in V_1 \rangle \text{ else } \langle V_2 \rangle_{n_1} \stackrel{\pi}{\leadsto} \langle (\psi_1 \uplus \psi_2) [n \mapsto e] \mid e_1 \vee e_2 \rangle_{n_2}}
$$

Reachability for programs $|V| \rightsquigarrow \psi$

program true ⊢ $\langle V \rangle_0 \stackrel{\pi_0}{\rightsquigarrow} \langle \psi | e \rangle_n$ $V \rightsquigarrow \psi$

Fig. 11. Rules for constructing the path reachability for each program point.

Figure [11](#page-15-0) gives the syntax-directed rules to construct such an environment. The main judgement is of the form $e + \langle V \rangle_n \stackrel{\pi}{\rightsquigarrow} \langle \psi | e' \rangle_n$ where *e* is a Boolean expression that needs to be satisfied for statement V to be executed, ψ is the reachability environment under-construction, and e' represents a Boolean condition that is true when the program has returned early. We write $V \rightsquigarrow \psi$ for "the reachability environment ψ is derived from V ". Reachability is affected by if statements, where the path condition for the program points in each branch statements are extended with the guard expression (negated for the **else** branch). Reachability also keeps track of the (negated) path conditions of preceding **return** statements, since instructions syntactically following a **return** statement may not execute if the function has returned early.

Invariant generation. The rules to construct invariants from Section [4.3](#page-11-0) are extended with predicates modeling all features, and extended with program point reachability, $\psi(\pi, n)$. Figure [12](#page-17-0) gives the revised rules for channel operations which deal with buffered and synchronous channels. In the unbuffered case, the channel state is constrained wrt. the number of concurrency operations performed as well as restrictions enforcing that at most one process is executing a rendezvous at any given point. Conversely, the invariant models the rendezvous instruction as unreachable for buffered channels.

In Figure [13,](#page-18-0) we introduce additional sets of syntactic occurrences to deal with waitgroup operations, conditionals, and return statements. The information about loops is extended to include waitgroup operations in their body. The sets W_W and W_A represent the occurrences of each w . Wait() and w . Add(e) operations, respectively, keeping track of the executing process, program point and, for the latter, the added expression e . Waitgroup invariants model the waitgroup counter relative to process states. The expression value of every Add operation outside a loop is added to the waitgroup counter, while expressions in loops are multiplied by the number of loop iterations. The invariant also models run-time errors caused by negative waitgroup counters. The set R contains all **return** statements, represented as process and program points pairs, (π, n) . The generated invariant $I_{ret}(r)$, $r \in \mathcal{R}$, models how a process executing a reachable return statement skips to termination without visiting any successor program points. The set I contains all **if** statements, represented as tuples (π, n_1, n_2, n_3, e) , where e is the guard expression, π the process, and program points n_1, n_2 and n_3 represent the guard program point, the beginning of the else branch, and the successor of the **if** statement, respectively. The generated invariant I _{if}(i), $i \in I$, asserts which branch program points are unreachable, depending on the value of the guard.

5.3 Preconditions

Figure [14](#page-19-0) details how the precondition predicate is extended to support waitgroups, as used in Strategies 2 and 3. Essentially, it generates an expression representing the sum of all expressions across add operations and requires this sum to amount to 0 for each waitgroup. This matches a programming pattern often observed in practice where w . Wait() is used only once, and syntactically preceded by all w . Add(e) operations.

6 Implementation

The approach has been implemented in the tool Ginger. It reuses Gomela to parse Go in the frontend and Dafny as a verifier in the back-end. We now present how the behaviors of Go program fragments are over-approximated with VirGo, and how to handle additional Go features.

6.1 Over-Approximating Go Programs with VirGo

To apply Ginger on real-world programs, they must be broken down into VirGo-expressible fragments. A program fragment is a set of related functions that cover the scope of a set of

 $\left($

Invariant generation with support for synchronous channels	
$o = (\pi, n, c, d)$ $o \notin O_L$	$o \in O_{\mathcal{L}}$
$I_{com}(o) = \textbf{if } \psi(\pi, n) \wedge n < P(\pi) \textbf{ then } 1 \textbf{ else } 0$	$Icom(o) = 0$
$o = (\pi, n, c, !)$ $o \notin O_{\mathcal{L}}$	$o \in O_{\mathcal{L}}$
$Isend(o) =$ if $\psi(\pi, n) \wedge$ $n < P(\pi)$ then 1 else 0 if $\psi(\pi, n) \wedge$ 1+n $< P(\pi)$ then 1 else 0	$Isend(o) = 0$
$o = (\pi, n, c, ?)$ $o \notin O_L$	$o\in O_{\mathcal{L}}$
$Irev(o) = \mathbf{if} \psi(\pi, n) \wedge n < P(\pi) \text{ then } 2 \text{ else } 0$	$Irev(o) = 0$
$l = (\pi, n, n', x, e, e', O, W)$ $N = \{\hat{n} \in N \mid (\pi, \hat{n}, c, d) \in O\}$ $E = \sum_{\hat{n} \in N} \text{if } \hat{n} < P(\pi) < n'$ then 1 else 0	
$IforCom(c, l, d) = \textbf{if } \psi(\pi, n) \textbf{ then } (x - e) * N + E \textbf{ else } 0$	
$l = (\pi, n, n', x, e, e', O, W)$ $N = \{\hat{n} \in N \mid (\pi, \hat{n}, c, !) \in O\}$ $E = \sum_{\hat{n} \in N} \text{ if } \hat{n} < P(\pi) < n' \text{ then } 1 \text{ else } 0$ $E = \sum_{\hat{n} \in N} \text{ if } 1 + \hat{n} < P(\pi) < n' \text{ then } 1 \text{ else } 0$ I forSend $(c, l) =$ if $\psi(\pi, n)$ then $(x - e) * N + E$ else 0	
$l = (\pi, n, n', x, e, e', O, W)$ $N = \{\hat{n} \in N \mid (\pi, \hat{n}, c, ?) \in O\}$ $E = \sum_{\hat{n} \in N} \mathbf{i} \mathbf{f} \hat{n} < P(\pi) < n'$ then 2 else 0	
I forRecv $(c, l) = \mathbf{if} \psi(\pi, n)$ then $(x - e) * N + E$ else 0	
$E(d) = \sum_{(\pi, n, c, d) \in O} I_{com}(\pi, n) + \sum_{l \in L} I_{forCom}(c, l, d)$ InoRendezvous = $\bigwedge_{(\pi, n, c, l) \in O} P(\pi) \neq n + 1$	
$\overline{Iasync(c)} = 0 \le c \le \kappa(c) \land c = E(!) - E(?) \land InoRendezvous$	
$E_1 = \sum_{(\pi, n, c, !) \in O} Isend(\pi, n) + \sum_{l \in L} Iforsend(c, l, d) \qquad E_2 = \sum_{(\pi, n, c, ?) \in O} Irev(\pi, n) + \sum_{l \in L} IforRecv(c, l, d)$ $ImaxOneRendezvous = \bigwedge_{(\pi, n, c, !) \in O} P(\pi) = n + 1 \implies \bigwedge_{(\pi', n', c, !) \in O} \pi' \neq \pi \implies P(\pi') \neq n' + 1$	
$Isync(c) = ACK \le c \le SND \land c = E1 - E2 \land ImaxOneRendezvous$	
$p = (\pi', n', c)$	
$Ichan(p) = \psi(\pi', n') \implies \text{if } \kappa(c) > 0 \text{ then } Iasync(c) \text{ else } Isync(c)$	

Fig. 12. Invariant generation rules for buffered and unbuffered channels operations.

concurrency primitives [\[8,](#page-24-2) [22,](#page-24-1) [38\]](#page-25-4). Functions are related if they are connected in the call graph and share concurrency primitives. A fragment is constructed from a function that allocates concurrency primitives. All callees that use concurrency primitives are transitively included. Concurrency

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Syntactic occurrences $wa = (\pi, n, w, e) \in W_{\mathcal{A}} \subseteq \Pi \times N \times W \times E$ Occurrences of w.Add(e) $ww = (\pi, n, w) \in W_w \subseteq \Pi \times N \times W$ Occurrences of w.Wait() $l = (..., W) \in \mathcal{L} \subseteq ... \times \wp(W_{\mathcal{A}})$ Occurrences of **for** extended with w.Add(e) $wa = (\pi, n, w, e) \in W_{\mathcal{AL}} = \begin{bmatrix} \end{bmatrix}$ w . Add(e) operations in loops $i = (\pi, n, n', n'', e) \in I \subseteq \Pi \times N \times N \times N \times E$ Occurrences of **if** $r = (\pi, n) \in \mathcal{R} \subseteq \Pi \times N$ Occurrences of **return** Invariant generation with support for waitgroups $wa = (\pi, n, w, e)$ $o \notin W_{\mathcal{AL}}$ $I_{add}(wa) = \textbf{if } \psi(\pi, n) \wedge n < P(\pi) \textbf{ then } e \textbf{ else } 0$ $wa \in W_{\mathcal{AL}}$ I add $(wa) = 0$ $l = (\pi, n, n', x, e, e', O, W)$ $A = \{(\hat{n}, \hat{e}) | (\pi, \hat{n}, w, e) \in W\}$ $E_1 = \sum_{(\pi, \hat{n}, w, \hat{e}) \in W} \hat{e}$ $E_2 = \sum_{(\hat{n}, \hat{e}) \in A} \textbf{if } \hat{n} < P(\pi) < n'$ then \hat{e} else 0 I forAdd $(w, l) =$ **if** $\psi(\pi, n)$ **then** $(x - e) * E_1 + E_2$ **else** 0 $E = \sum_{l \in \mathcal{L}} I$ forAdd (w, l) + $\sum_{(\pi, n, w, e) \in \mathcal{W}_{\mathcal{A}}} I$ add (π, n) $I_{\text{wg}}(w) = 0 \leq w \wedge w = E$ $r = (\pi, n)$ $I_{ret}(r) = \psi(\pi, n) \implies \frac{1}{n} < P(\pi) < T(\pi)$ $g = (\pi, n, \pi')$ $I_{\mathcal{B}}(g) = \psi(\pi, n) \land n < \mathsf{P}(\pi) \iff \mathsf{P}(\pi') \neq -1$ $i = (\pi, n_1, n_2, n_3, e)$ I *if* (*i*) = **if** e then $!(n_2 \le P(\pi) < n_3)$ else $!(n_1 < P(\pi) < n_2)$ $l = (\pi, n, n', x, e, e', O, W)$ I for(l) = **if** $\psi(\pi, n) \wedge e \leq e'$ **then** $e \le x \le e' \land (P(\pi) < n \implies x = e) \land (n < P(\pi) < n' \implies x < e')$ \wedge $(n' \leq P(\pi) \implies x = e')$

Fig. 13. Invariant generation rules for waitgroups.

else $x = e \wedge !(n < P(\pi) < n')$

parameters are recorded while the fragment is being identified. Fragments are ruled out if precision is degraded in the discovery phase due to, e.g., escaping channels.

We adopt Gomela's front-end to excise each fragment from its calling context, identify concurrency parameters, and strip it of non-concurrent operations. These fragments are passed to Ginger for VirGo translation. The crux of the translation technique is to over-approximate the Go semantics by preserving the control flow and the interactions with concurrency primitives but

Precondition generation with support for waitgroups							
$l = (\pi, n_1, n_2, x, e_1, e_2, O, W)$							
$for(w, l) = \sum_{(\pi, n, w, e) \in W} e * (\text{if } \psi(\pi, n_1) \wedge e_1 < e_2 \text{ then } e_2 - e_1 \text{ else } 0)$							
$wg(w) = \sum_{(\pi, n, w, e) \in W_{\mathcal{A}} \backslash W_{\mathcal{A}}_f} e + \sum_{l \in \mathcal{L}} for(w, l)$	$V \Longrightarrow E_{\kappa}$ balanced(V) = \bigwedge wg(w) = 0 $w \in W$						

Fig. 14. Precondition generation for waitgroups.

abstracting away from non-concurrency related constructs, following techniques used in [\[8,](#page-24-2) [17,](#page-24-5) [18\]](#page-24-7). Note that since VirGo does not support named function calls and definitions, all process spawning and function calls are inlined. Additionally, we perform two syntactical transformation on the internal representation of Go programs to widen the applicability of Ginger.

First, to support the pattern of spawning goroutines in loops, we "commute" nested for-go patterns to go-for whenever possible, i.e., we transform the fragment on the left below to the one on the right.

```
for 0.. x { go { P } } \implies go { for 0.. x { P } }
```
When P is $c!$;...;c! or $c?$;...;c?, this transformation is sound since we transform k identical processes each performing *n* identical operations into one process performing $k \cdot n$ operations. The transformation can also be soundly applied when P executes "atomically", e.g., if P is mu.lock(); Q; mu.unlock() (for some Q), and mu.unlock() does not appear anywhere else in the fragment (incl. Q). Ginger raises an alarm when the transformation cannot be applied soundly.

Second, to provide partial support for conditionals in loops, we merge branches whenever possible, i.e., when they perform the same sequence of concurrent actions. For instance, we transform the fragment on the left below to the one on the right while preserving behavior equivalence.

for 0.. x { **if** e { c ! } **else** { c ! } } \implies **for** 0.. x { c ! }

6.2 Additional Language Features

The VIRGo language, as presented in Figure [2,](#page-3-1) is fully supported by the translation to COREDAFNY implemented in Ginger.

As described in Section [5,](#page-12-0) synchronous channels (with capacity 0) are encoded using an additional alternative for send and receive operations. For each operation on c, if $\kappa(c) = 0$ then we use an additional rendezvous state to encode synchronous communication. Waitgroup operations are encoded by allowing w . Add(e) to increment the corresponding state variable by e , and w . Wait() by blocking until the counter is set to 0. We also support standard mutexes by encoding them as channels of capacity one. If-then-else are supported by mapping each program point to a conjunction of conditionals that must be met for the instruction to be reachable. Every variable in conditionals that does not correspond to known concurrency parameters produces a fresh Dafny variable. Return statements are encoded as a jump to the termination point of the relevant process ($T(\pi)$). Using these techniques, GINGER can analyze programs such as the fragment in Figure [1](#page-2-0) (right), which includes a conditional and a return statement. We use a similar technique to support standard patterns such as blocking operations with timeouts, e.g., the Go program on the left becomes the VirGo program on the right below:

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Primitive	Fragments	Expressible	First successful strategy			
			Total	Strat. 1	Strat. 2	Strat. 3
Channels	258	159 (61.6%)	157	41	116	
WaitGroup	205	171 (83.4%)	170	86	67	17
Both	120	48 (40%)	46	18	25	3
Total	583	378 (64.8%)	373	145	208	20

Table 1. Expressible fragments, and results of their verification.

select {

```
case res := <- c1 :...
case <-time. After (time. Second):... }
                                                   =⇒
                                                              if timeout_123 { c1?;... }
                                                              else { ... }
```
The generated precondition may constrain fresh variable timeout_123.

7 Evaluation

We evaluate the approach by running GINGER on code fragments extracted from the large enterprise code base of Uber, which includes thousands of thoroughly tested microservices. The code base consists of ∼3 million LoC across packages that contain concurrency features. From these packages, we extracted 583 parameterized fragments ($|\mathbb{X}| > 0$) discovered using a simple syntactical analysis on the code base. The syntactical analysis selects fragments that are parameterized (they include parameterized loop bounds or a concurrency operation such as **make**(**chan** T, k) or wg.Add(k), where k is a variable). Overall these parameterized fragments total 30,777 lines of code, where individual fragments are 53 lines on average, ranging from 7 to 309 lines. The number of distinct concurrency parameters per fragment has an average of 1.08 and ranges from 1 to 3, and concurrency parameters are used 2.20 times on average, between 1 and 6 times. Although typical fragments are small, they often exhibit complex concurrent behavior as in the motivating example from Section [2](#page-2-5) and Examples [7.1](#page-21-0) and [7.2](#page-21-1) below.

We answer the following questions:

RQ1 How many parametric fragments can be expressed in VirGo via Ginger?

RQ2 How many expressible fragments can be successfully verified?

RQ3 How much CPU time does it take to verify VirGo encodings?

[RQ1](#page-20-1) - VirGo expressivity Table [1](#page-20-2) shows the expressivity of VirGo wrt. the 583 parameterized fragments we extracted from Uber's code base. We classify fragments based on the types of concurrency primitives affected by parameters and measure the relative number of VirGo-expressible fragments. Note that 294 (out of 378) fragments are expressible thanks to a sound application of the go-for commute transformation; 32 additional fragments necessitated a potentially unsound go-for commute transformation. For all of these we manually confirmed that the analysis is sound. When translation fails, it is due to unsupported features, e.g., general select statements, or unsupported loops (nested, non-terminating, etc.). Features such as premature loop exit (via **break** or **return**) are suitable candidates for future work, while others are more challenging, e.g., non-deterministic **select** statements in loops.

[RQ2](#page-20-3) - Verifiability Table [1](#page-20-2) also shows the precondition strategy that succeeds first, applied in the same order as presented in Section [4.4.](#page-12-1) The number of fragments verifiable with Strategy 1 relative to Strategies 2 and 3 suggests that developers often write fragments with some implicit assumptions. Strategies 2 and 3 help make these assumptions explicit. Suggested preconditions validated by Strategy 2 can be used by the developer to devise counter-examples e.g., by challenging assumptions about the sign of values used for channel capacities. When the verification fails with Strategy 2,

```
1 done = chan [0];
2 go { done !; };
3 go {
4 for 0 .. readers / 2 {
5 done !;
6 }
7 };
8 go { done !; };
9 go {
10 for readers / 2 .. readers {
11 done !;
12 }
13 }
14 go {
15 for 0 .. readers + 2 {
16 done ?;
17 }
18 }
                                            1 if numLevels == 0 { return } else { skip }
                                            2 w = WaitGroup;
                                            3 c = chan [ numLevels ];
                                            4 for 0 .. complianceLevels { w . Add (1); };
                                            5 go {
                                            6 for 0 .. complianceLevels {
                                            7 c!:
                                           8 \text{ w. Add } (-1);
                                            9 }
                                          10 };
                                          11 w. Wait();
                                          12 for 0 .. numLevels {
                                           13 c ?;
                                           14 }
```
(a) Successfully verified example.

(b) Example that fails to verify.

Fig. 15. Complex examples of VirGo-expressible programs.

the invariant constructed in Section [4.3](#page-11-0) is too weak to support the proof. Failure to verify with any strategy is either caused by unsuccessful applications of the go-for commute transformation presented in Section [6.2](#page-19-1) (three cases) or by loss of vital constraints over concurrency parameters in the front-end.

Although we manually validated the fragments verified by Ginger, the impact of these bugs on the overall system's functionality would need to be evaluated in a wider context, and ideally in conjunction with the developers. Additionally, our manual validation process, may not capture all contextual dependencies and interactions within the entire code base, potentially overlooking the broader implications of the detected bugs.

Next we discuss two VirGo programs (adapted from representative Go code) that highlight the challenge addressed by the technique.

Example 7.1. Figure [15a](#page-21-2) is a program for which the absence of partial deadlocks depends the value of readers. At first glance, the number of send operations on done appears equal to the number of receive operations, making the program partial deadlock free. However, readers corresponds to an unconstrained function parameter, and thus may be instantiated to a negative number, in which case there will be unmatched (blocking) send operations.

If the number of loop iterations is defined as iter(x, y), where iter(x, y) = $max(y - x, 0)$, GINGER infers the following precondition and validates it with Strategy 2:

 $iter(0, readers/2) + iter(readers/2, readers) + 2 = iter(0, readers + 2)$

When readers is negative, the equality may not be satisfied (e.g., if readers = -2 the formula reduces to 2=0), and some of the sending goroutines get stuck.

For this example, Goat [\[38\]](#page-25-4) and GCatch [\[22\]](#page-24-1) unconditionally report partial deadlocks as they either over-approximate or unsoundly fix the number of loop iterations without considering the arithmetic connection between loop bounds. By contrast, Gomela [\[8\]](#page-24-2) fails to detect the partial deadlock because readers is not tested by default for negative values.

Example 7.2. The program in Figure [15b](#page-21-2) is an example of a correct program that GINGER is unable to verify. In the source program, complianceLevels is assigned to numLevels, but the front-end fails

Table 2. Verification time per CoreDaFny program per strategy (including failures). All experiments were performed on a MacBook Pro™ with a M1 Pro CPU (10 cores), and 32GB of memory.

to capture this equality. For this reason, all precondition strategies fail because send operations cannot be unblocked by receive operations, as all receive operations may only be executed after waiting on the waitgroup has been unblocked. The inferred *balanced* precondition is:

 $0 <$ numLevels \le complianceLevels \le numLevels + numLevels

It is easy to see that this condition is satisfied when numLevels == complianceLevels. As in the previous example, other tools fail to precisely reason about this example, as it is parameterized. Notably Gomela reports that some valuations fail and others succeed.

[RQ3](#page-20-4) - Performance In Table [2,](#page-22-1) we measure the verification time for each precondition strategy. We omit the execution times of the Gomela and Ginger translation phases as they are negligible. The majority of examples can be verified in under four seconds. This shows the technique can be applied to many real-world examples in a practical amount of time.

8 Related Work

Several approaches have been developed to detect or rule out bugs in concurrent Go programs. Besides the static verifiers we mentioned earlier (Goat [\[38\]](#page-25-4), Gomela [\[8\]](#page-24-2), GCatch [\[22\]](#page-24-1)), earlier works include static checkers based on behavioral types [\[11,](#page-24-6) [17,](#page-24-5) [18,](#page-24-7) [27\]](#page-25-5), abstract interpretation [\[24\]](#page-25-7), and forkable regular expressions [\[34\]](#page-25-8). Except Gomela, which instantiates concurrency parameters based on user-provided values, all these approaches over-approximate concurrency parameters, or otherwise rely on unsound heuristics to handle imprecision introduced by loops, failing to perform a trustworthy analysis of Figure [1.](#page-2-0) Additionally, they cannot suggest preconditions that enforce partial deadlock freedom. Another static verifier for Go programs is Gobra [\[39\]](#page-25-9), an extension of the Viper [\[26\]](#page-25-10) framework, which enables the verification of programs for memory safety and partial correctness based on user-provided specifications (e.g., pre- and post-condition annotations within Go Programs). Gobra cannot detect blocking bugs such as partial deadlocks.

Dynamic partial deadlock detection techniques have also been proposed, such as [\[35\]](#page-25-11), and GoLeak and LeakProf [\[30\]](#page-25-2), two dynamic tools introduced by Uber that leverage an extensive testing and profiling infrastructure. GFuzz [\[21\]](#page-24-8) relies on random exploration of test execution paths by fuzzing select case choices. Yuan et al. [\[40\]](#page-25-12) have curated a benchmark suite of Go programs that targets dynamic verifiers and does not include parameterized communication patterns.

The automated generation of invariants has been studied extensively with particular focus on (sequential) transition systems rather than concurrent program fragments. Well-known techniques such as Craig interpolation [\[14,](#page-24-9) [23\]](#page-25-13) and IC3 [\[3\]](#page-23-0) (aka. PDR [\[9\]](#page-24-10)) require exact knowledge of the underlying transition system, with the latter notably being adapted for software verification [\[5\]](#page-23-1). Some techniques rely on the existence of templates from which to derive invariants [\[4,](#page-23-2) [6,](#page-23-3) [33\]](#page-25-14). Other techniques use a black-box approach based on learning [\[12,](#page-24-11) [28\]](#page-25-15) or large language models [\[15\]](#page-24-12). The discovery of invariants at runtime has also been studied, most notably with Daikon [\[10\]](#page-24-13) which aims at discovering a (sequential) program's likely invariants by analyzing its executions. Our

technique uses a white-box approach and generates invariants following patterns informed by the specific communication mechanisms and their usage in Go. The techniques above could complete ours when it fails to generate a strong enough invariant.

Parameterized verification [\[2\]](#page-23-4) addresses the analysis of concurrent system with an arbitrary number of processes, where some verification techniques generate invariants, e.g., [\[29\]](#page-25-16). While VirGo does not natively allow the arbitrary spawning of processes, it supports arbitrary parameters for loop bounds, path conditions and communication primitives.

Dafny has generally been used to write correct-by-construction programs, including concurrent systems [\[20\]](#page-24-14), but not often as the backend verifier. We conjecture that using Dafny's backend, Boogie [\[1\]](#page-23-5), would improve CPU-time at the cost of reduced transparency for the intermediate steps. The Civl verifier [\[16\]](#page-24-15) (a recent extension of Boogie) could serve as an alternative back-end. Civl natively supports concurrency, and hence might allow us to simplify our encoding.

9 Conclusion

We have presented a novel approach for automatically verifying partial deadlock freedom in program fragments that feature parametric communication, by translating Go fragments to Dafny programs. The experimental results demonstrate that the approach can be used on an enterprise code base to verify a large number of such fragments that are out of reach for other verification techniques. For future work, it may be interesting to explore opportunities for generalizing the approach to cover a larger family of parameterized fragments and to adapt the approach to related languages that support channel-based process communication, such as Rust and Kotlin.

Data Availability Statement

The source code of Ginger is available at [https://github.com/VladSaioc/ginger.](https://github.com/VladSaioc/ginger) An artifact that contains Ginger, the illustrated examples, and some additional fragments representative of real life coding patterns is archived on Zenodo [\[32\]](#page-25-17). Due to their proprietary nature, the fragments used in the evaluation are not publicly available.

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